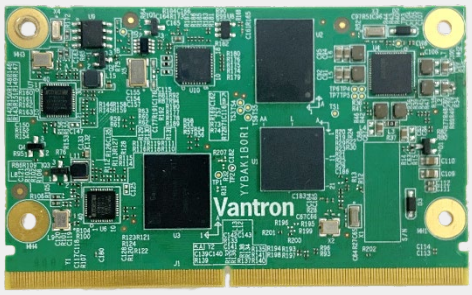
VT-SBC-SMARC-IMX93

Computer-on-Module



Product Brief

VT-SBC-SMARC-IMX93 Computer-on-Module (CoM) comes in a SMARC 2.1 form factor for easy integration into customers' development environment. It is powered by the NXP i.MX9331/9332 processor that integrates the scalable Arm Cortex-A55 core with a frequency up to 1.7GHz. Through integration of the features of the processor such as efficient machine learning acceleration, advanced security features, and optimal energy efficiency, the module is designed for smart home, IoT edge, Automotive, and industrial applications.

The module supports a rich set of interfaces, including multiple displays in the form of LVDS and MIPI DSI, high-speed serial buses like CAN, I^2C, UART, SPI, and USB 2.0, as well as diverse peripheral options such as GPIO and MIPI CSI. In addition, the module offers two gigabit Ethernet for connectivity. With extensive scalability options, the module reduces the development time and costs for customers.

Exterior and Features

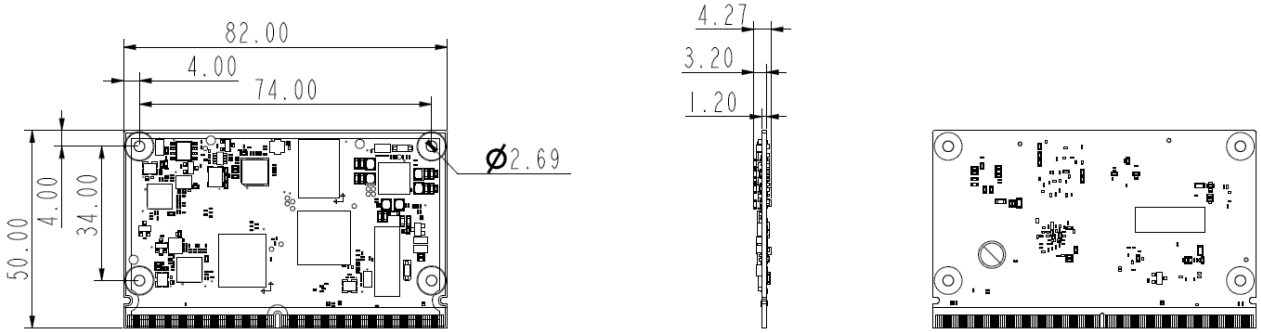


VT-SBC-SMARC-IMX93
NXP i.MX9331/9332 processor
Linux operating system
MIPI DSI and LVDS for video output
Rich interfaces for flexible expansion
Gigabit Ethernet
USB 2.0 Host and OTG
-40℃ ~ +85℃ operating temperature
SMARC 2.1 form factor

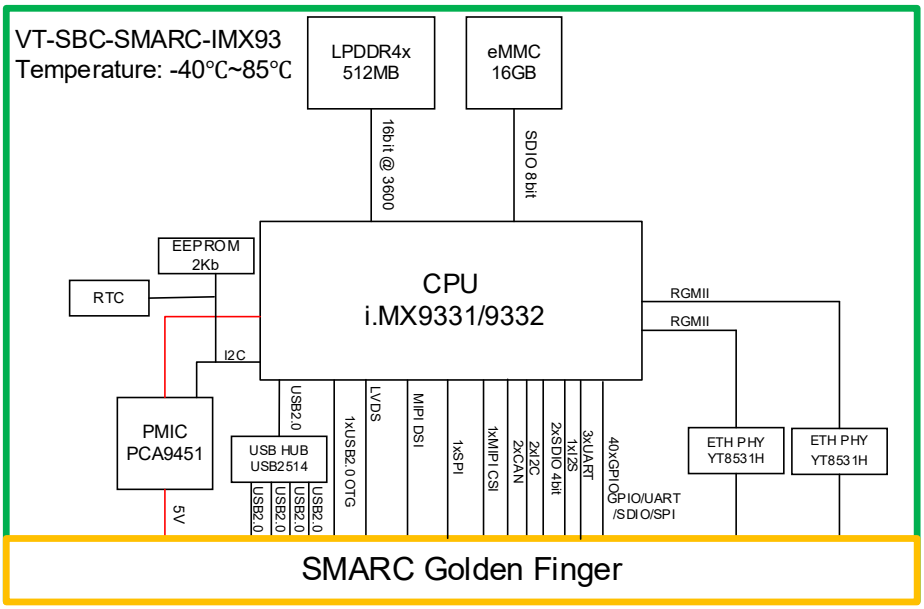
VT-SBC-SMARC-IMX93 Computer-on-Module Datasheet

VT-SBC-SMARC-IMX93			
System	CPU	NXP i.MX9331, Single-core ARM Cortex-A55, up to 1.7GHz / NXP i.MX9332, Dual-core ARM Cortex-A55, up to 1.7GHz	
	Memory	512MB LPDDR4 (Optional: 2GB/8GB)	
	Storage	16GB eMMC 5.1	
	PMIC	PCA9451	
Communication	Ethernet	2 × 10/100/1000Mbps Ethernet	
Media	Display	1 × 4-Lane MIPI DSI	1 × 4-Lane LVDS
	Camera	1 × 2-Lane MIPI CSI	
	Audio	3 × ADC input	
		1 × I^2S	
I/O	Serial	1 × Debug UART	
		3 × Communication UART (including one with flow control)	
	USB	1 × USB 2.0 OTG	4 × USB 2.0 Host
	GPIO	Up to 40 × GPIO (GPIO/UART/SDIO/SPI)	
	SPI	1 × SPI	
	SDIO	2 × 4-bit SDIO	
	Expansion Bus	2 × I^2C	2 × CAN
	Key	1 × Power key	
	RTC	Supported	
	WDT	Supported	
Power	Input	5V/2A DC	
Software	Operating System	Linux	
Mechanical	Dimensions	82mm × 50mm SMARC 2.1	
Environmental Condition	Temperature	Operating: -40℃~+85℃	Storage: -40℃~+85℃
	Humidity	5%-95% RH (non-condensing)	
	Certification	CE	

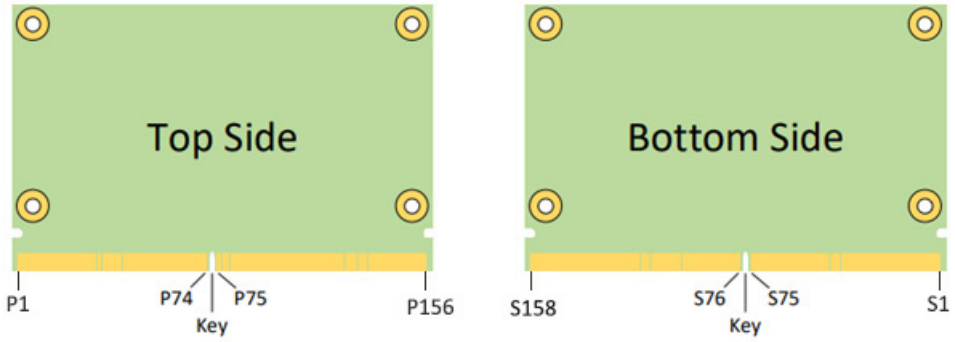
Mechanical Outline



Block Diagram



Edge Connector Pinout



Primary (Top) Side	Function	I/O	I/O Level	Description
P1	NC	-	-	No connection
P2	NC	-	-	No connection
P3	NC	-	-	No connection
P4	NC	-	-	No connection
P5	NC	-	-	No connection
P6	NC	-	-	No connection
P7	NC	-	-	No connection
P8	NC	-	-	No connection
P9	NC	-	-	No connection
P10	NC	-	-	No connection
P11	NC	-	-	No connection
P12	NC	-	-	No connection
P13	NC	-	-	No connection
P14	NC	-	-	No connection

Primary (Top) Side	Function	I/O	I/O Level	Description
P15	NC	-	-	No connection
P16	NC	-	-	No connection
P17	NC	-	-	No connection
P18	NC	-	-	No connection
P19	ENET0_PHY_TRXN3	I/O		Differential pair signals for external transformer
P20	ENET0_PHY_TRXP3	I/O		
P21	NC	-	-	No connection
P22	LED0_LINK1000	O	3.3V	Link speed indication LED for ENET0 1000Mbps
P23	ENET0_PHY_TRXN2	I/O		Differential pair signals for external transformer
P24	ENET0_PHY_TRXP2	I/O		
P25	LED0_ACT	O	3.3V	Link / Activity indication LED driven low on link (10, 100 or 1000 Mbps) blinks on activity
P26	ENET0_PHY_TRXN1	I/O		Differential pair signals for external transformer
P27	ENET0_PHY_TRXP1	I/O		
P28	GBE0_CTREF	NC		No connection
P29	ENET0_PHY_TRXN0	I/O		Differential pair signals for external transformer
P30	ENET0_PHY_TRXP0	I/O		
P31	NC	-	-	No connection
P32	GND			Ground
P33	NC	-	-	No connection
P34	SD2_CMD	I/O	1.8V	SD 2 command response
P35	SD2_nCD	I	1.8V	SD 2 card detection
P36	SD2_CLK	O	1.8V	SD 2 clock
P37	SD2_PWR_EN	O	1.8V	SD 2 power enabling
P38	GND			Ground
P39	SD2_DATA0	I/O	1.8V	SD 2 data line
P40	SD2_DATA1	I/O	1.8V	SD 2 data line
P41	SD2_DATA2	I/O	1.8V	SD 2 data line
P42	SD2_DATA3	I/O	1.8V	SD 2 data line
P43	ECSPI1_SS0	O	1.8V	SPI 0 master chip select 0
P44	ECSPI1_SCLK	O	1.8V	SPI0 clock
P45	ECSPI1_MISO	I	1.8V	SPI 0 master input slave output
P46	ECSPI1_MOSI	O	1.8V	SPI 0 master output slave input
P47	GND	P		Ground
P48	NC	-	-	No connection
P49	NC	-	-	No connection
P50	NC	-	-	No connection
P51	NC	-	-	No connection
P52	NC	-	-	No connection
P53	NC	-	-	No connection
P54	SD3_CMD	O	1.8V	SD 3 command response
P55	NC	-	-	No connection
P56	SD3_CLK	O	1.8V	SD 3 clock
P57	SD3_DATA1	I	1.8V	SD 3 data line
P58	SD3_DATA0	O	1.8V	SD 3 data line
P59	GND	P	1.8V	Ground
P60	USB0_DP	I/O	USB	USB 0 differential data pairs
P61	USB0_DN	I/O	USB	

Primary (Top) Side	Function	I/O	I/O Level	Description
P62	NC	-	-	No connection
P63	USB0_VBUS_DET	I	USB VBUS 5V	USB 0 host power detection
P64	USB0_ID	I	3.3V	USB 0 OTG detection foot
P65	USB_HUB_D2+	I/O		USB 2 differential data pairs
P66	USB_HUB_D2-	I/O		
P67	USB_H1_EN	I/O	3.3V (PU 100K)	USB 1 enable
P68	GND	p		Ground
P69	USB_HUB_D4+	I/O		USB 4 differential data pairs
P70	USB_HUB_D4-	I/O		
P71	USB_H2_EN	I/O	3.3V (PU 100K)	USB 2 enable
P72	NC	-	-	No connection
P73	NC	-	-	No connection
P74	USB_H3_EN	I/O	3.3V (PU 100K)	USB 3 enable
P75	NC	-	-	No connection
P76	NC	-	-	No connection
P77	NC	-	-	No connection
P78	NC	-	-	No connection
P79	NC	-	-	No connection
P80	NC	-	-	No connection
P81	NC	-	-	No connection
P82	NC	-	-	No connection
P83	NC	-	-	No connection
P84	NC	-	-	No connection
P85	NC	-	-	No connection
P86	NC	-	-	No connection
P87	NC	-	-	No connection
P88	NC	-	-	No connection
P89	NC	-	-	No connection
P90	NC	-	-	No connection
P91	NC	-	-	No connection
P92	NC	-	-	No connection
P93	NC	-	-	No connection
P94	NC	-	-	No connection
P95	NC	-	-	No connection
P96	NC	-	-	No connection
P97	NC	-	-	No connection
P98	NC	-	-	No connection
P99	NC	-	-	No connection
P100	NC	-	-	No connection
P101	NC	-	-	No connection
P102	NC	-	-	No connection
P103	NC	-	-	No connection
P104	NC	-	-	No connection
P105	NC	-	-	No connection
P106	NC	-	-	No connection
P107	NC	-	-	No connection
P108	GPIO0/CAM0_PWR#	O	1.8V	GPIO

Primary (Top) Side	Function	I/O	I/O Level	Description
P109	GPIO2/CAM0_RST#	O	1.8V	GPIO
P110	NC	-	-	No connection
P111	NC	-	-	No connection
P112	NC	-	-	No connection
P113	NC	-	-	No connection
P114	NC	-	-	No connection
P115	NC	-	-	No connection
P116	S_GPIO10	I/O	1.8V	GPIO
P117	NC	-	-	No connection
P118	NC	-	-	No connection
P119	NC	-	-	No connection
P120	NC	-	-	No connection
P121	I2C1_PM_CL	O	1.8V	I^2C clock for power management
P122	I2C1_PM_DATA	I/O	1.8V	I^2C data for power management
P123	BOOT_SEL0#	I	1.8V	Boot mode and CFG switch
P124	BOOT_SEL1#	I	1.8V	Boot mode and CFG switch
P125	BOOT_SEL2#	I	1.8V	Boot mode and CFG switch
P126	NC	-	-	No connection
P127	RESET_IN#	I	1.8V	System reset
P128	POWER_BTN#	I	1.8V	Power on/off
P129	UART2_TXD	O	1.8V	UART 2 transmit data
P130	UART2_RXD	I	1.8V	UART 2 receive data
P131	NC	-	-	No connection
P132	NC	-	-	No connection
P133	NC	-	-	No connection
P134	UART1_TXD	O	1.8V	UART 1 transmit data
P135	UART1_RXD	I	1.8V	UART 1 receive data
P136	UART3_TXD	O	1.8V	UART 3 transmit data
P137	UART3_RXD	I	1.8V	UART 3 receive data
P138	UART3_RTS#	O	1.8V	UART 3 request to send
P139	UART3_CTS#	I	1.8V	UART 3 clear to send
P140	UART5_TXD	O	1.8V	UART 5 transmit data
P141	UART5_RXD	I	1.8V	UART 5 receive data
P142	NC	-	-	No connection
P143	FLEXCAN1_TX	O	1.8V	CAN 1 transmit output
P144	FLEXCAN1_RX	I	1.8V	CAN 1 receive output
P145	FLEXCAN2_TX	O	1.8V	CAN 2 transmit output
P146	FLEXCAN2_RX	I	1.8V	CAN 2 receive output
P147	VDD_IN	P	5.0V	5V Power in
P148	VDD_IN	P	5.0V	5V Power in
P149	VDD_IN	P	5.0V	5V Power in
P150	VDD_IN	P	5.0V	5V Power in
P151	VDD_IN	P	5.0V	5V Power in
P152	VDD_IN	P	5.0V	5V Power in
P153	VDD_IN	P	5.0V	5V Power in
P154	VDD_IN	P	5.0V	5V Power in
P155	VDD_IN	P	5.0V	5V Power in

Primary (Top) Side	Function	I/O	I/O Level	Description
P156	VDD_IN	P	5.0V	5V Power in

Secondary (Bottom) Side	Function	I/O	I/O Level	Description
S1	NC	-	-	No connection
S2	NC	-	-	No connection
S3	GND	P		Ground
S4	I2S2_MCLK			I^2S 2 master clock
S5	I2C3_CAM0_CK	O	1.8V (PU 2.2K)	I^2C 3 clock
S6	CAM_MCK	O	1.8V	Camera master clock
S7	I2C3_CAM0_DAT	O	1.8V (PU 2.2K)	I^2C 3 data
S8	CSI_CLK_P	O	-	MIPI CSI differential clock +
S9	CSI_CLK_N	O	-	MIPI CSI differential clock -
S10	GND	P		Ground
S11	CSI_D0_P	I	-	MIPI CSI receive data 0 +
S12	CSI_D0_N	I	-	MIPI CSI receive data 0 -
S13	GND	p		Ground
S14	CSI_D1_P	I	-	MIPI CSI receive data 1 +
S15	CSI_D1_N	I	-	MIPI CSI receive data 1 -
S16	GND	P		Ground
S17	ENET1_PHY_TRXN0	I/O		Differential pair signals for external transformer
S18	ENET1_PHY_TRXP0	I/O		
S19	NC	-	-	No connection
S20	ENET1_PHY_TRXN1	I/O		Differential pair signals for external transformer
S21	ENET1_PHY_TRXP1	I/O		
S22	ENET1_LED_LINK1000	O	3.3V	Link Speed indication LED for ENET1 1000Mbps
S23	ENET1_PHY_TRXN2	I/O		Differential pair signals for external transformer
S24	ENET1_PHY_TRXP2	I/O		
S25	GND	P		Ground
S26	ENET1_PHY_TRXN3	I/O		Differential pair signals for external transformer
S27	ENET1_PHY_TRXP3	I/O		
S28	GBE1_CTREF	NC		No connection
S29	NC	-	-	No connection
S30	NC	-	-	No connection
S31	LED_ACT1	O	3.3V	Link / Activity indication LED driven low on link (10, 100 or 1000 Mbps) blinks on activity
S32	NC	-	-	No connection
S33	NC	-	-	No connection
S34	NC	-	-	No connection
S35	USB_HUB_D1+	I/O		USB 1 differential data pairs
S36	USB_HUB_D1+	I/O		
S37	NC	-	-	No connection
S38	NC	-	-	No connection
S39	NC	-	-	No connection
S40	NC	-	-	No connection
S41	NC	-	-	No connection
S42	NC	-	-	No connection
S43	NC	-	-	No connection

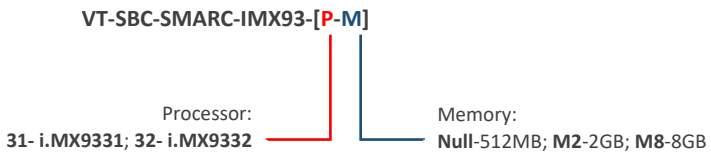
Secondary (Bottom) Side	Function	I/O	I/O Level	Description
S44	NC	-	-	No connection
S45	NC	-	-	No connection
S46	NC	-	-	No connection
S47	NC	-	-	No connection
S48	I2C_GP_CK	O	1.8V (PU 2.2K)	I^2C clock
S49	I2C_GP_DAT	I/O	1.8V (PU 2.2K)	I^2C data
S50	I2S2_LRCK	I/O	1.8V	I^2S 2 left & right synchronization clock
S51	I2S2_SDOUT	O	1.8V	I^2S 2 digital audio output
S52	I2S2_SDIN	I	1.8V	I^2S 2 digital audio input
S53	I2S2_CK	I/O	1.8V	I^2S 2 digital audio clock
S54	NC	-	-	No connection
S55	NC	-	-	No connection
S56	SD3_DATA2	I/O	1.8V	SD 3 data line
S57	SD3_DATA3	I/O	1.8V	SD 3 data line
S58	NC	-	-	No connection
S59	NC	-	-	No connection
S60	NC	-	-	No connection
S61	NC	-	-	No connection
S62	NC	-	-	No connection
S63	NC	-	-	No connection
S64	NC	-	-	No connection
S65	NC	-	-	No connection
S66	NC	-	-	No connection
S67	NC	-	-	No connection
S68	USB_DP_DN3	I/O	USB	USB 3 differential data pairs
S69	USB_DM_DN3	I/O	USB	
S70	NC	-	-	No connection
S71	NC	-	-	No connection
S72	NC	-	-	No connection
S73	NC	-	-	No connection
S74	NC	-	-	No connection
S75	NC	-	-	No connection
S76	NC	-	-	No connection
S77	NC	-	-	No connection
S78	NC	-	-	No connection
S79	NC	-	-	No connection
S80	NC	-	-	No connection
S81	NC	-	-	No connection
S82	NC	-	-	No connection
S83	NC	-	-	No connection
S84	NC	-	-	No connection
S85	NC	-	-	No connection
S86	NC	-	-	No connection
S87	NC	-	-	No connection
S88	NC	-	-	No connection
S89	NC	-	-	No connection
S90	NC	-	-	No connection

Secondary (Bottom) Side	Function	I/O	I/O Level	Description
S91	NC	-	-	No connection
S92	NC	-	-	No connection
S93	NC	-	-	No connection
S94	NC	-	-	No connection
S95	NC	-	-	No connection
S96	NC	-	-	No connection
S97	NC	-	-	No connection
S98	NC	-	-	No connection
S99	NC	-	-	No connection
S100	NC	-	-	No connection
S101	NC	-	-	No connection
S102	NC	-	-	No connection
S103	NC	-	-	No connection
S104	NC	-	-	No connection
S105	NC	-	-	No connection
S106	NC	-	-	No connection
S107	LCD1_BKLT_EN	O	1.8V	LCD 1 backlight enable
S108	DSI_CKP	-	-	MIPI DSI clock +
S109	DSI_CKN	-	-	MIPI DSI clock -
S110	GND			Ground
S111	DSI_DP0	-	-	MIPI DSI data 0 +
S112	DSI_DN0	-	-	MIPI DSI data 0 -
S113	NC	-	-	No connection
S114	DSI_DP1	-	-	MIPI DSI data 1 +
S115	DSI_DN1	-	-	MIPI DSI data 1 -
S116	LCD1_VDD_EN	O	1.8V	LCD 1 power enable
S117	DSI_DP2	-	-	MIPI DSI data 2+
S118	DSI_DN2	-	-	MIPI DSI data 2-
S119	GND			Ground
S120	DSI_DP3	-	-	MIPI DSI data 3+
S121	DSI_DN3	-	-	MIPI DSI data 3-
S122	LCD1_BKLT_PWM	O	1.8V	LCD 1 backlight PWM control
S123	S_GPIO13	I/O	1.8V	GPIO
S124	GND			Ground
S125	LVDS_TX0_P	-	-	LVDS data 0 +
S126	LVDS_TX0_N	-	-	LVDS data 0 -
S127	LCD0_BKLT_EN	O	1.8V	LCD 0 backlight enable
S128	NC	-	-	No connection
S129	NC	-	-	No connection
S130	GND			Ground
S131	NC	-	-	No connection
S132	NC	-	-	No connection
S133	LCD0_VDD_EN	O	1.8V	LCD 0 power enable
S134	LVDS_CLK_P	-	-	LVDS clock +
S135	LVDS_CLK_N	-	-	LVDS clock -
S136	GND			Ground
S137	LVDS_TX3_P	-	-	LVDS data 3 +

Secondary (Bottom) Side	Function	I/O	I/O Level	Description
S138	LVDS_TX3_N	-	-	LVDS data 3 -
S139	I2C_LCD_CK	I/O	1.8V (PU 2.2K)	I2C clock
S140	I2C_LCD_DAT	I/O	1.8V (PU 2.2K)	I2C data
S141	LCD0_BKLT_PWM	O	1.8V	LCD 0 backlight PWM control
S142	S_GPIO12	I/O	1.8V	GPIO
S143	NC	-	-	No connection
S144	NC	-	-	No connection
S145	WDT_TIME_OUT	O	1.8V	Watchdog output
S146	NC	-	-	No connection
S147	RTC_VBAT	P	3.3V	RTC power
S148	TAMPER0			Tamper-proof detection signal 0
S149	TAMPER1			Tamper-proof detection signal 1
S150	NC	-	-	No connection
S151	ADC_IN1	I		ADC input 1
S152	ADC_IN2	I		ADC input 2
S153	ADC_IN3	I		ADC input 3
S154	NC	-	-	No connection
S155	NC	-	-	No connection
S156	USB_H4_EN	I/O	3.3V (PU 100K)	USB 4 enable
S157	NC	-	-	No connection
S158	NC	-	-	No connection

Ordering Information

Nomenclature



Example Ordering No.	Processor	Memory	Description
VT-SBC-SMAR-IMX93-31	NXP i.MX9331	512MB	i.MX9331, 512MB memory, 16GB storage
VT-SBC-SMAR-IMX93-31M2	NXP i.MX9331	2GB	i.MX9331, 2GB memory, 16GB storage
VT-SBC-SMAR-IMX93-32M8	NXP i.MX9332	8GB	i.MX9332, 8GB memory, 16GB storage

Packing List	
VT-SBC-SMARC-IMX93 computer-on-module	1

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