VT-SBC-SMARC-EKT

Computer-on-Module



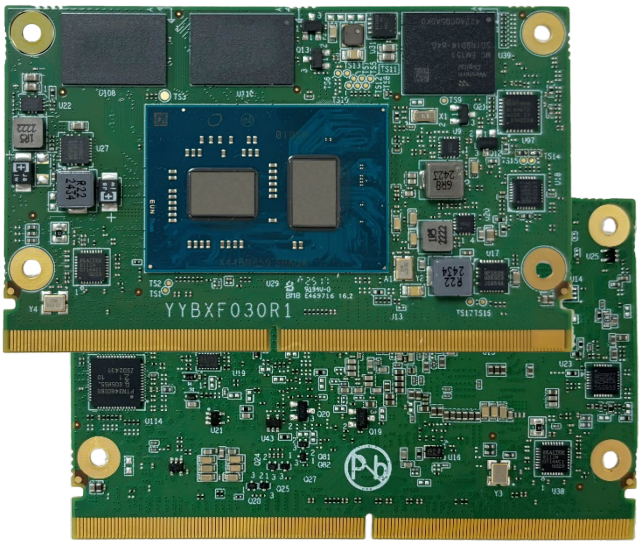
Product Brief

VT-SBC-SMARC-EKT Computer-on-Module (CoM) comes in a SMARC 2.1.1 form factor for easy integration into customers' development environment. It is powered by the Intel® Elkhart Lake high-performance processor that has four cores capable of delivering high computing power for various embedded applications at low thermal design power (TDP). Equipped with UHD Graphics GPU for 10th Gen Intel® processors, it supports up to 4096 x 2160 @60Hz triple display output, with full compatibility for advanced graphics APIs such as OpenGL, OpenCL, DirectX, and Intel® Quick Sync Video, providing exceptional graphics performance for a range of applications.

VT-SBC-SMARC-EKT offers rich configurable I/Os for flexible user expansion, including USB, UART, SPI, I^2C, and PCIe, ensuring high-speed data transfer and reliable communication with peripherals. For connectivity, the module offers two MDI, supporting 10/100/1000Mbps Ethernet networking.

VT-SBC-SMARC-EKT is designed for a broad spectrum of applications, including smart factory automation, industrial control systems, medical devices, digital signage, and edge computing platforms. Its robust performance and wide range of I/O options ensure it can handle demanding tasks while maintaining energy efficiency, making it a perfect solution for modern embedded system designs.

Exterior and Features



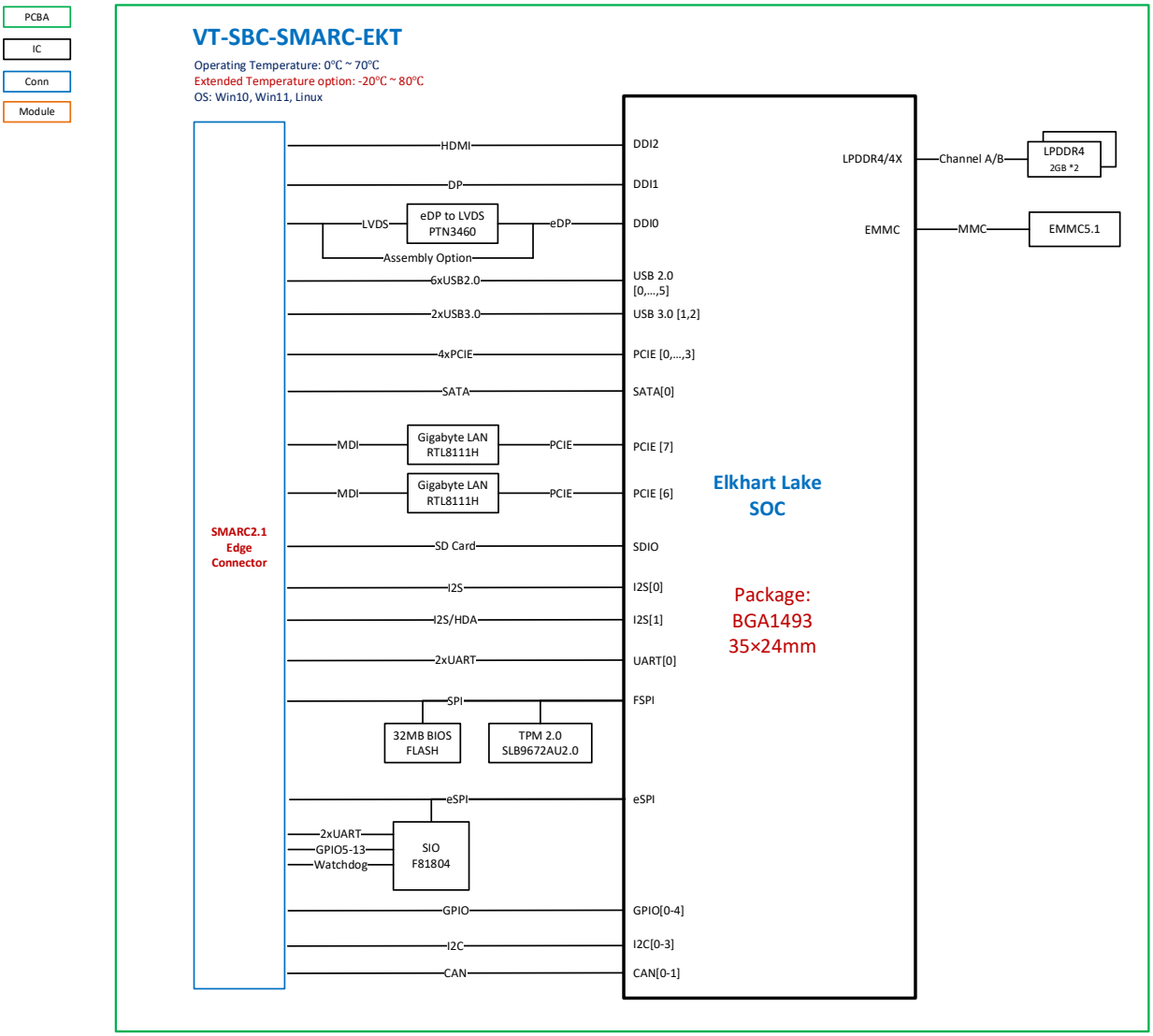
VT-SBC-SMARC-EKT
Intel® Elkhart Lake high-performance processor
On-board 4GB memory + 64GB storage
HDMI 1.4b/DP++/eDP/LVDS for video output
Rich interface signals for flexible expansion
Gigabit Ethernet connectivity
Multi-OS support: Windows 10/11 IoT, Ubuntu 20.04
Extended & non-extended temperature options
SMARC 2.1.1 form factor for flexible integration

VT-SBC-SMARC-EKT Computer-on-Module Datasheet

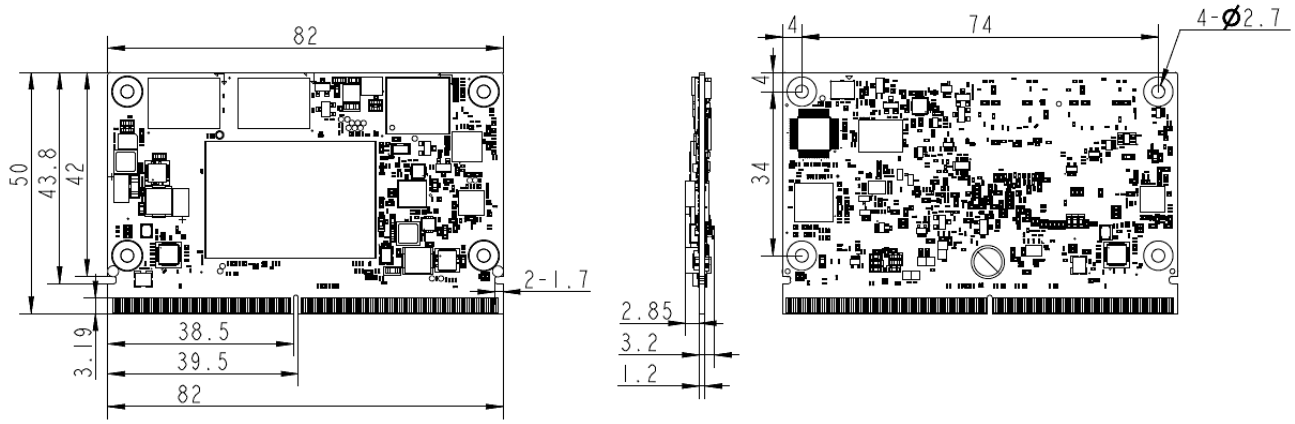
Specifications		
System	CPU	Intel® Atom® x6425E Processor, 4C4T, 2.0GHz (base), 3.0GHz (turbo), 12W TDP / Intel® Celeron® J6412 Processor, 4C4T, 2.0GHz (base), 2.6GHz (turbo), 10W TDP
	GPU	UHD Graphics for 10th Gen Intel® processors, support OpenGL, OpenCL, DirectX, Intel® Quick Sync Video
	AI Acceleration	Intel® Gaussian & Neural Accelerator (GNA) for audio and speech-centric AI workloads
	Memory	4GB LPDDR4 (up to 16GB)
	Storage	64GB eMMC V5.1 (Up to 256GB)
Media	Video processing	4K @60Hz HEVC (H.265)/AVC (H.264) decoder, 4K @60Hz HEVC (H.265)/AVC (H.264) encoder
Miscellaneous	RTC	Supported (power from the carrier board)
	Watchdog timer	Supported
	Security	TPM 2.0
Power	Input	4.75V-5.25V DC
	Consumption	Typical: 1.5W (power-on without applications running)
Software	Operating system	Windows 10/11 IoT, Ubuntu 20.04
	Applications	SDK available
Mechanical	Dimensions	82mm x 50mm x 6.05mm (SMARC 2.1.1 form factor)
Environmental Condition	Temperature	Operating: -20°C ~ +80°C (6425E); 0 ~ +70°C (J6412) Storage: -40°C ~ +85°C
	Humidity	Operating & Storage: 10%~85%RH (Non-condensing)
	Certification	CE

I/O		
Display (Support up to 3 displays in extended mode)	1 x HDMI 1.4b, up to 4096 x 2160 @60Hz	
	1 x DP++, up to 4096 x 2160 @60Hz	
	1 x eDP/Dual LVDS, up to 4096 x 2160 @60Hz	
Audio	1 x I^2S	1 x I^2S/HDA
Ethernet	2 x MDI, 10/100/1000Mbps	
USB	2 x USB 3.0 host	6 x USB 2.0 host
UART	4 x Communication UART	
CAN	2 x CAN (for x6425E running Ubuntu 20.04)	
GPIO	13 x GPIO (Max.)	
SPI	1 x SPI	1 x eSPI
I^2C	4 x I^2C	
PCIe	4 x PCIe 3.0 (1 x4 / 1 x2 + 2 x1 / 4 x1)	
SATA	1 x SATA	
SDIO	1 x 4-Bit SDIO	
System Control	1 x Power button	1 x Reset button
	1 x Power indicator	3 x Programmable indicator

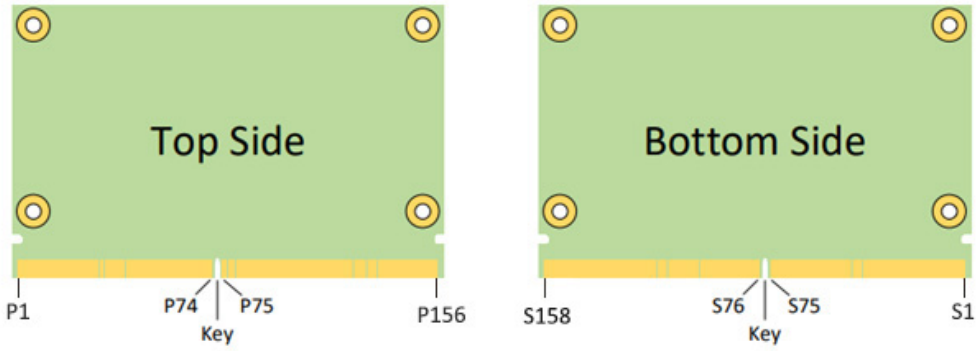
Block Diagram



Product Outlines



Edge Connector Pinout



Primary (Top) Side	Function 1*	Function 2	Level	Description
P1	GP_C02	-	-	SMBus alert
P2	GND	-	-	Ground
P3	NC	-	-	No connection
P4	NC	-	-	No connection
P5	NC	-	-	No connection
P6	NC	-	-	No connection
P7	NC	-	-	No connection
P8	NC	-	-	No connection
P9	GND	-	-	Ground
P10	NC	-	-	No connection
P11	NC	-	-	No connection
P12	GND	-	-	Ground
P13	NC	-	-	No connection
P14	NC	-	-	No connection
P15	GND	-	-	Ground
P16	NC	-	-	No connection
P17	NC	-	-	No connection
P18	GND	-	-	Ground
P19	L1_MDI_3N	-	-	PHY 1 media dependent interface 3-
P20	L1_MDI_3P	-	-	PHY 1 media dependent interface 3+
P21	L1_LINK_100#	-	-	PHY 1 100Mbps link indicator
P22	L1_LINK_1000#	-	-	PHY 1 1000Mbps link indicator
P23	L1_MDI_2N	-	-	PHY 1 media dependent interface 2-
P24	L1_MDI_2P	-	-	PHY 1 media dependent interface 2+
P25	L1_LINK_ACT#	-	-	PHY 1 activity indicator
P26	L1_MDI_1N	-	-	PHY 1 media dependent interface 1-
P27	L1_MDI_1P	-	-	PHY 1 media dependent interface 1+

* Function 1 is set as the default. For specific pins, there are multiple functions available.

Primary (Top) Side	Function 1	Function 2	Level	Description
P28	GBE0_CTREF	-	-	Center-tap reference voltage for carrier board Ethernet magnetics (if required by the GBE PHY module)
P29	PHY0_MDI0-	-	-	PHY 0 media dependent interface 0-
P30	PHY0_MDI0+	-	-	PHY 0 media dependent interface 0+
P31	SPI0_CS1_N	-	-	SPI 0 chip select 1-
P32	GND	-	-	Ground
P33	SD_SDIO_WP	-	-	SDIO 0 write protect
P34	SD_SDIO_CMD	-	-	SDIO 0 command
P35	SD_SDIO_CD	-	-	SDIO 0 card detection
P36	SD_SDIO_CLK	-	-	SDIO 0 clock
P37	SD_SDIO_PWR	-	-	SDIO 0 power enable
P38	GND	-	-	Ground
P39	SD_SDIO_D0	-	-	SDIO 0 data 0
P40	SD_SDIO_D1	-	-	SDIO 0 data 1
P41	SD_SDIO_D2	-	-	SDIO 0 data 2
P42	SD_SDIO_D3	-	-	SDIO 0 data 3
P43	NC	-	-	No connection
P44	SPI0_CLK_R	-	1.8V	SPI 0 clock
P45	SPI0_IO1_MISO_R	-	1.8V	SPI 0 master in slave out
P46	SPI0_IO0_MOSI_R	-	1.8V	SPI 0 master out slave in
P47	GND	-	-	Ground
P48	SATA_TXP0	-	-	SATA transmit +
P49	SATA_TXN0	-	-	SATA transmit -
P50	GND	-	-	Ground
P51	SATA_RXP0	-	-	SATA receive +
P52	SATA_RXN0	-	-	SATA receive +
P53	GND	-	-	Ground
P54	NC	-	-	No connection
P55	ESPI_CS1_N	-	1.8V	eSPI chip select 1
P56	ESPI_CLK	-	1.8V	eSPI clock
P57	ESPI_IO_1	-	1.8V	eSPI IO 1
P58	ESPI_IO_0	-	1.8V	eSPI IO 0
P59	GND	-	-	Ground
P60	USB2.0_DP5	-	-	USB 2.0_5 data +
P61	USB2.0_DN5	-	-	USB 2.0_5 data -
P62	USB2.0_OC0#_3.3V	-	-	USB 2.0 over current protection 0
P63	NC	-	-	No connection
P64	NC	-	-	No connection
P65	USB2.0_DP1	-	-	USB 2.0_1 data +
P66	USB2.0_DN1	-	-	USB 2.0_1 data -

Primary (Top) Side	Function 1	Function 2	Level	Description
P67	USB2.0_OC2#_3.3V	-	-	USB 2.0 over current protection 2
P68	GND	-	-	Ground
P69	USB2.0_DP0	-	-	USB 2.0_0 data +
P70	USB2.0_DN0	-	-	USB 2.0_0 data -
P71	USB2.0_OC3#_3.3V	-	-	USB 2.0 over current protection 3
P72	NC	-	-	No connection
P73	NC	-	-	No connection
P74	USB2.0_OC0#_3.3V	-	-	USB 2.0 over current protection 0
		KEY		
P75	BUF_PLT_RST_N	-	-	Platform reset
P76	USB2.0_OC2#_3.3V	-	-	USB 2.0 over current protection 2
P77	PCIE_CLKREQ1#	-	-	PCIe 1 clock request
P78	PCIE_CLKREQ0#	-	-	PCIe 0 clock request
P79	GND	-	-	Ground
P80	PCIE_CLK_P2	-	-	PCIe 2 reference clock +
P81	PCIE_CLK_N2	-	-	PCIe 2 reference clock -
P82	GND	-	-	Ground
P83	PCIE_CLK_P0	-	-	PCIe 0 reference clock +
P84	PCIE_CLK_N0	-	-	PCIe 0 reference clock -
P85	GND	-	-	Ground
P86	PCIE_RXP_A	-	-	PCIe 0 receiver +
P87	PCIE_RXN_A	-	-	PCIe 0 receiver -
P88	GND	-	-	Ground
P89	PCIE_TXP_A	-	-	PCIe 0 transmitter +
P90	PCIE_TXN_A	-	-	PCIe 0 transmitter -
P91	GND	-	-	Ground
P92	HDMI_DATA2+	-	-	HDMI transmit data 2 +
P93	HDMI_DATA2-	-	-	HDMI transmit data 2 -
P94	GND	-	-	Ground
P95	HDMI_DATA1+	-	-	HDMI transmit data 1 +
P96	HDMI_DATA1-	-	-	HDMI transmit data 1 -
P97	GND	-	-	Ground
P98	HDMI_DATA0+	-	-	HDMI transmit data 0 +
P99	HDMI_DATA0-	-	-	HDMI transmit data 0 -
P100	GND	-	-	Ground
P101	HDMI_CLK+	-	-	HDMI clock +
P102	HDMI_CLK-	-	-	HDMI clock -
P103	GND	-	-	Ground
P104	HDMI_HPD#	-	-	HDMI hot plug detect input

Primary (Top) Side	Function 1	Function 2	Level	Description
P105	DDI2_DDC_SCL	-	-	HDMI DDC clock
P106	DDI2_DDC_SDA	-	-	HDMI DDC data
P107	NC	-	-	No connection
P108	GPIO0	CAM0_PWR#	1.8V	GPIO 0
P109	GPIO1	CAM1_PWR#	1.8V	GPIO 1
P110	GPIO2	CAM0_RST#	1.8V	GPIO 2
P111	GPIO3	CAM1_RST#	1.8V	GPIO 3
P112	HD_RST#	GPIO4	1.8V	HDA reset
P113	GIPO5	PWM	3.3V	GPIO 5
P114	GIPO6	TACHIN	3.3V	GPIO 6
P115	GPIO07	-	3.3V	GPIO 7
P116	GPIO08	-	3.3V	GPIO 8
P117	GPIO09	-	3.3V	GPIO 9
P118	GPIO10	-	3.3V	GPIO 10
P119	GPIO11	-	3.3V	GPIO 11
P120	GND	-	-	Ground
P121	SMB_HOST_EP_CLK	-	1.8V	SMBus host clock
P122	SMB_HOST_EP_DATA	-	1.8V	SMBus host data
P123	NC	-	-	No connection
P124	NC	-	-	No connection
P125	NC	-	-	No connection
P126	NC	-	-	No connection
P127	RESET_OUT#	-	-	System reset out
P128	RESET_IN#	-	-	System reset in
P129	UART1_TXD	-	-	UART 1 transmit data
P130	UART1_RXD	-	-	UART 1 receive data
P131	UART1_RTS	-	-	UART 1 request to send
P132	UART1_CTS	-	-	UART 1 clear to send
P133	GND	-	-	Ground
P134	SOC_UART0_TXD	-	-	CPU UART 0 transmit data
P135	SOC_UART0_RXD	-	-	CPU UART 0 receive data
P136	UART2_TXD	-	-	UART 2 transmit data
P137	UART2_RXD	-	-	UART 2 receive data
P138	UART2_RTS	-	-	UART 2 request to send
P139	UART2_CTS	-	-	UART 2 clear to send
P140	SOC_UART1_TX	-	-	CPU UART 1 transmit data
P141	SOC_UART1_RX	-	-	CPU UART 1 receive data

Primary (Top) Side	Function 1	Function 2	Level	Description
P142	GND	-	-	Ground
P143	SOC_CAN0_TX	-	-	CAN 0 transmit data (x6425E)
P144	SOC_CAN0_RX	-	-	CAN 0 receive data (x6425E)
P145	SOC_CAN1_TX	-	-	CAN 1 transmit data (x6425E)
P146	SOC_CAN1_RX	-	-	CAN 1 receive data (x6425E)
P147	+V5A	-	-	5V power input
P148	+V5A	-	-	5V power input
P149	+V5A	-	-	5V power input
P150	+V5A	-	-	5V power input
P151	+V5A	-	-	5V power input
P152	+V5A	-	-	5V power input
P153	+V5A	-	-	5V power input
P154	+V5A	-	-	5V power input
P155	+V5A	-	-	5V power input
P156	+V5A	-	-	5V power input

Secondary (Bottom) Side	Function 1	Function 2	Level	Description
S1	SIO_I2C2_SCL	-	1.8V	SIO I^2C 2 clock
S2	SIO_I2C2_SDA	-	1.8V	SIO I^2C 2 data
S3	GND	-	-	Ground
S4	NC	-	-	No connection
S5	SIO_I2C1_SCL	-	1.8V	SIO I^2C 1 clock
S6	NC	-	-	No connection
S7	SIO_I2C1_SDA	-	1.8V	SIO I^2C 1 data
S8	NC	-	-	No connection
S9	NC	-	-	No connection
S10	GND	-	-	Ground
S11	NC	-	-	No connection
S12	NC	-	-	No connection
S13	GND	-	-	Ground
S14	NC	-	-	No connection
S15	NC	-	-	No connection

Secondary (Bottom) Side	Function 1	Function 2	Level	Description
S16	GND	-	-	Ground
S17	L2_MDI_0P	-	-	PHY 2 media dependent interface 0+
S18	L2_MDI_0N	-	-	PHY 2 media dependent interface 0-
S19	L2_LINK_100#	-	-	PHY 2 100Mbps link indicator
S20	L2_MDI_1P	-	-	PHY 2 media dependent interface 1+
S21	L2_MDI_1N	-	-	PHY 2 media dependent interface 1-
S22	L2_LINK_1000#	-	-	PHY 2 1000Mbps link indicator
S23	L2_MDI_2P	-	-	PHY 2 media dependent interface 2+
S24	L2_MDI_2N	-	-	PHY 2 media dependent interface 2-
S25	GND	-	-	Ground
S26	L2_MDI_3P	-	-	PHY 2 media dependent interface 3+
S27	L2_MDI_3N	-	-	PHY 2 media dependent interface 3-
S28	GBE1_CTREF	-	-	Center-tap reference voltage for carrier board Ethernet magnetics (if required by the GBE PHY module)
S29	PCIE_TXP_D	-	-	PCIe 3 transmitter +
S30	PCIE_TXN_D	-	-	PCIe 3 transmitter -
S31	L2_LINK_ACT#	-	-	PHY 2 activity indicator
S32	PCIE_RXP_D	-	-	PCIe 3 receiver +
S33	PCIE_RXN_D	-	-	PCIe 3 receiver -
S34	GND	-	-	Ground
S35	USB2.0_DP4	-	-	USB 2.0_4 data +
S36	USB2.0_DN4	-	-	USB 2.0_4 data -
S37	NC	-	-	No connection
S38	I2S3_MCLK2	-	1.8V	I^2S 3 master clock
S39	I2S3_SFRM	-	1.8V	I^2S 3 frame sync
S40	I2S3_TXD	-	1.8V	I^2S 3 serial data output
S41	I2S3_RXD	-	1.8V	I^2S 3 serial data input
S42	I2S3_SCLK	-	1.8V	I^2S 3 serial clock
S43	ESPI_ALERT1	-	-	eSPI alert 1
S44	ESPI_ALERT2	-	-	eSPI alert 2
S45	NC	-	-	No connection
S46	NC	-	-	No connection
S47	GND	-	-	Ground
S48	SIO_I2C4_SCL		1.8V	SIO I^2C 4 clock
S49	SIO_I2C4_SDA		1.8V	SIO I^2C 4 data
S50	HD_SYNC	-	1.8V	HDA sync
S51	HD_SDOUT	-	1.8V	HDA data output

Secondary (Bottom) Side	Function 1	Function 2	Level	Description
S52	HD_SDIN0_R	-	1.8V	HDA data input
S53	HD_BITCLK	-	1.8V	HDA bit clock
S54	SATA_LED#	-	-	SATA LED
S55	USB2.0_OC3#_3.3V	-	-	USB 2.0 over current protection 3
S56	ESPI_IO_2	-	-	eSPI IO 2
S57	ESPI_IO_3	-	-	eSPI IO 3
S58	ESPI_RESET_N	-	-	eSPI reset
S59	USB2.0_DP2	-	-	USB 2.0_2 data +
S60	USB2.0_DN2	-	-	USB 2.0_2 data -
S61	GND	-	-	Ground
S62	USB3.0_TXP0	-	-	USB 3.0_0 transmit +
S63	USB3.0_TXN0	-	-	USB 3.0_0 transmit -
S64	GND	-	-	Ground
S65	USB3.0_RXP0	-	-	USB 3.0_0 receive +
S66	USB3.0_RXN0	-	-	USB 3.0_0 receive -
S67	GND	-	-	Ground
S68	USB2.0_DP3	-	-	USB 2.0_3 data +
S69	USB2.0_DN3	-	-	USB 2.0_3 data -
S70	GND	-	-	Ground
S71	USB3.0_TXP1	-	-	USB 3.0_1 transmit +
S72	USB3.0_TXN1	-	-	USB 3.0_1 transmit -
S73	GND	-	-	Ground
S74	USB3.0_RXP1	-	-	USB 3.0_1 receive +
S75	USB3.0_RXN1	-	-	USB 3.0_1 receive -
KEY				
S76	BUF_PLT_RST_N	-	-	Platform reset
S77	BUF_PLT_RST_N	-	-	Platform reset
S78	PCIE_RXP_C	-	-	PCIe 2 receiver +
S79	PCIE_RXN_C	-	-	PCIe 2 receiver -
S80	GND	-	-	Ground
S81	PCIE_TXP_C	-	-	PCIe 2 transmitter +
S82	PCIE_TXN_C	-	-	PCIe 2 transmitter -
S83	GND	-	-	Ground
S84	PCIE_CLK_P1	-	-	PCIe reference clock 1 +
S85	PCIE_CLK_N1	-	-	PCIe reference clock 1 -
S86	GND	-	-	Ground
S87	PCIE_RXP_B	-	-	PCIe 1 receiver +
S88	PCIE_RXN_B	-	-	PCIe 1 receiver -
S89	GND	-	-	Ground

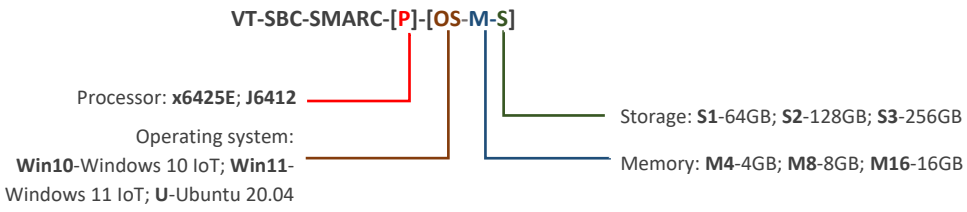
Secondary (Bottom) Side	Function 1	Function 2	Level	Description
S90	PCIE_TXP_B	-	-	PCIe 1 transmitter +
S91	PCIE_TXN_B	-	-	PCIe 1 transmitter -
S92	GND	-	-	Ground
S93	DP_LANE0+	-	-	DisplayPort (DP) lane 0 +
S94	DP_LANE0-	-	-	DisplayPort (DP) lane 0 -
S95	DP0_AUX_SEL	-	-	DisplayPort (DP) 0 auxiliary channel
S96	DP_LANE1+	-	-	DisplayPort (DP) lane 1 +
S97	DP_LANE1-	-	-	DisplayPort (DP) lane 1 -
S98	DP_HPD	-	-	DisplayPort (DP) hot plug detection
S99	DP_LANE2+	-	-	DisplayPort (DP) lane 2 +
S100	DP_LANE2-	-	-	DisplayPort (DP) lane 2 -
S101	NC	-	-	No connection
S102	DP_LANE3+	-	-	DisplayPort (DP) lane 3 +
S103	DP_LANE3-	-	-	DisplayPort (DP) lane 3 -
S104	NC	-	-	No connection
S105	DP++_AUXP			DP++ auxiliary channel +
S106	DP++_AUXN			DP++ auxiliary channel -
S107	NC	-	-	No connection
S108	LVDS_B_CLK+	-	-	LVDS_B clock +
S109	LVDS_B_CLK-	-	-	LVDS_B clock -
S110	GND	-	-	Ground
S111	LVDS_B_D0+	-	-	LVDS_B data 0 +
S112	LVDS_B_D0-	-	-	LVDS_B data 0 -
S113	NC	-	-	No connection
S114	LVDS_B_D1+	-	-	LVDS_B data 1 +
S115	LVDS_B_D1-	-	-	LVDS_B data 1 -
S116	NC	-	-	No connection
S117	LVDS_B_D2+	-	-	LVDS_B data 2 +
S118	LVDS_B_D2-	-	-	LVDS_B data 2 -
S119	GND	-	-	Ground
S120	LVDS_B_D3+	-	-	LVDS_B data 3 +
S121	LVDS_B_D3-	-	-	LVDS_B data 3 -
S122	NC	-	-	No connection
S123	GPIO13	-	1.8V	GPIO 13
S124	GND	-	-	Ground
S125	LVDS_A_D0+	eDP	-	LVDS_A data 0 +
S126	LVDS_A_D0-	eDP	-	LVDS_A data 0 -

Secondary (Bottom) Side	Function 1	Function 2	Level	Description
S127	LCD_BKLTEN	-	3.3V	Backlight enable
S128	LVDS_A_D1+	eDP	-	LVDS_A data 1 +
S129	LVDS_A_D1-	eDP	-	LVDS_A data 1 -
S130	GND	-	-	Ground
S131	LVDS_A_D2+	eDP	-	LVDS_A data 2 +
S132	LVDS_A_D2-	eDP	-	LVDS_A data 2 -
S133	LCD0_VDD_EN	-	3.3V	LCD VDD enable
S134	LVDS_A_CLK+	eDP	-	LVDS_A clock +
S135	LVDS_A_CLK-	eDP	-	LVDS_A clock 0 -
S136	GND	-	-	Ground
S137	LVDS_A_D3+	eDP	-	LVDS_A data 3 +
S138	LVDS_A_D3-	eDP	-	LVDS_A data 3 -
S139	SIO_I2C3_SCL	-	-	I^2C 3 clock for LCD
S140	SIO_I2C3_SDA	-	-	I^2C 3 data for LCD
S141	LCD0_BKLT_PWM	-	3.3V	LCD backlight PWM control
S142	GPIO12	-	-	GPIO 12
S143	GND	-	-	Ground
S144	eDP0_HPD	-	-	eDP hot plug detection
S145	SYS_RESET#	-	-	Watchdog output
S146	PCIE_WAKE3#_3.3V	-	-	PCIe wake up
S147	+V3.3A_RTC	-	-	RTC battery power supply
S148	LID#	-	-	Lid open/close indication to Module. Low indicates lid closure (which system may use to initiate a sleep state). Carrier to float the line in in-active state. Active low, level sensitive. Should be de-bounced on the Module.
S149	SLEEP#	-	-	Sleep indicator from the carrier board. May be sourced from user Sleep button or Carrier logic. Carrier to float the line in in-active state. Active low, level sensitive. Should be de-bounced on the Module.
S150	NC	-	-	No connection
S151	NC	-	-	No connection
S152	NC	-	-	No connection
S153	PM_SLP_S3_N	-	-	The Module shall drive this signal low when the system is in a standby power state.

Secondary (Bottom) Side	Function 1	Function 2	Level	Description
S154	PM_RSMRST_N	-	-	Carrier board circuits (apart from power management and power path circuits) should not be powered up until the module asserts the CARRIER_PWR_ON signal.
S155	SARADC_VIN1_KEY/RECOVERY	-	-	SARADC_VIN1_KEY/RECOVERY
S156	BATLOW#	-	-	Battery low indication
S157	NC	-	-	No connection
S158	NC	-	-	No connection

Ordering Information

Nomenclature



Example Ordering No.	Processor	Operating System	Memory	Storage
VT-SBC-SMAR-x6425E-Win10M4S2	Intel® Atom® x6425E	Windows 10 IoT	4GB	128GB
VT-SBC-SMARC-J6412-UM16S3	Intel® Celeron® J6412	Ubuntu 20.04	16GB	256GB
VT-SBC-SMARC-J6412-Win11M8S1	Intel® Celeron® J6412	Windows 11 IoT	8GB	64GB

Packing List	
VT-SBC-SMARC-EKT computer-on-module	1

Since its establishment in 2002 by two Silicon Valley entrepreneurs, Vantron Technology has been at the forefront of the connected IoT devices and IoT platform solutions. Today, Vantron boasts a global customer base that includes many Fortune Global 500 companies. Its product lines cover edge intelligent hardware, IoT communication devices, industrial displays and BlueSphere cloud platforms.

With over 20 years of experience in R&D of intelligent edge hardware, Vantron has provided users with diverse embedded solutions featuring ARM and X86 architectures. Its offerings range from Linux, Android to Windows, from embedded to desktop level, and from gateways to servers. In addition, it provides users with system trimming, driver transplantation and more to cater to the unique needs of its users.

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