VT-SBC-SMARC-8MP

Computer-on-Module

Product Brief Introduction

The VT-SBC-SMARC-8MP computer-on-module adopts the SMARC form factor for seamless system integration. Powered by NXP's versatile high-performance i.MX8M Plus processor, it supports multi-display output, high-speed peripheral interfaces and diverse storage interfaces. Equipped with both wired and wireless networking capabilities, it delivers secure data handling. Furthermore, this module supports stable industrial control connectivity, catering to Industry 4.0 use cases such as smart retail, self-service kiosks, factory automation, intelligent medical devices, and digital signage.

Boasting high flexibility and robust performance, the module operates reliably across an extended industrial temperature range of -20℃ to +70℃, with an optional wide-range variant rated for -40℃ to +85℃, positioning it as a dependable industrial IoT platform.

Exterior and Features

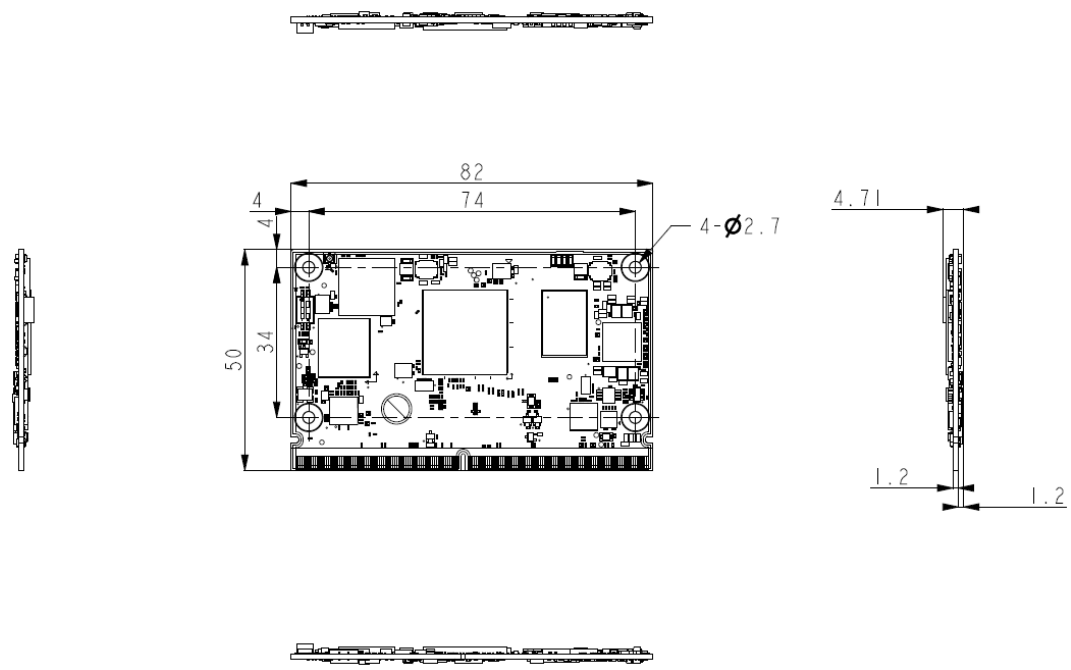
VT-SBC-SMARC-8MP
NXP i.MX8M Plus high-performance processor
Up to 2.3 TOPS NPU for edge AI inference
2GB LPDDR4, 16GB storage
1080p60 H.265/H.264 video codec, dual-display support
Rich interfaces, flexible expansion
Dual GbE, Wi-Fi & BT connectivity
Android & Linux Yocto supported
Wide temperature range supported
SMARC 2.1 compliant for easy integration

VT-SBC-SMARC-8MP Computer-on-Module Datasheet

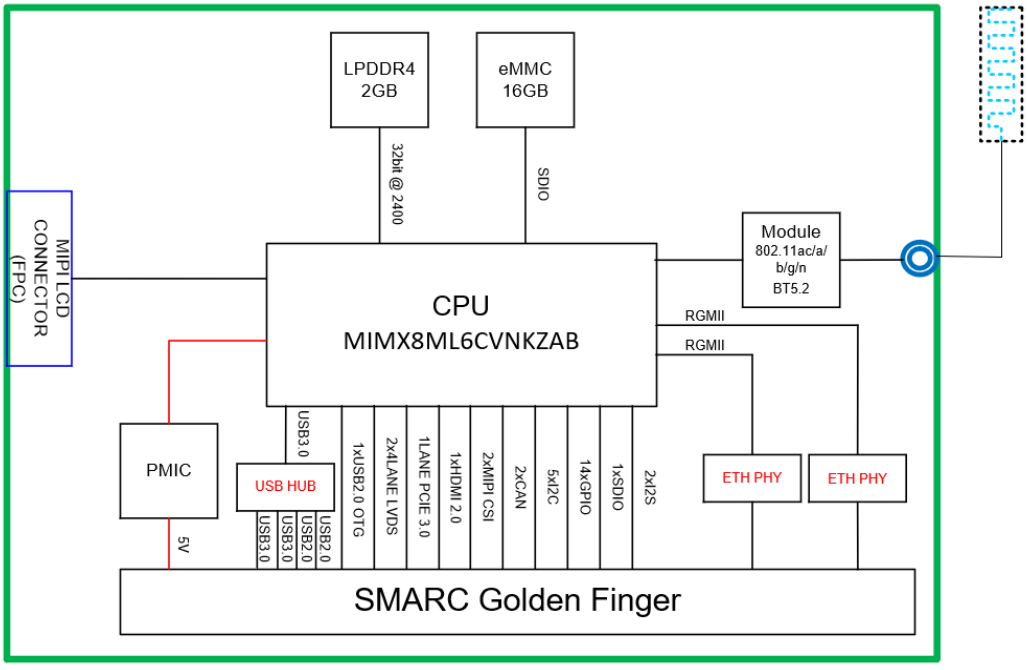
Specifications			
System	CPU	NXP i.MX8M Plus, Quad-core ARM Cortex-A53, up to 1.6GHz Single-core ARM Cortex-M7 real-time MCU, 800MHz	
	GPU	Vivante GC7000UL(3D)/GC502L(2D), supports OpenGL ES 3.1, Vulkan, OpenCL 1.2	
	NPU	2.3 TOPS, supports word detect, noise reduction, beamforming, speech/image recognition	
	Memory	2GB LPDDR4	
	Storage	16GB eMMC 5.1	
Media	Video Processing	1080p60 H.265/H.264 video codec	
	Image Processing	375 Mpixel/s HDR ISP, supports 12MP@30fps, 4kp45, 2 x 1080p80 configurations	
Power	Input	5V 2A DC	
	Consumption	Max. 10W (Full load)	
Software	Operating System	Android, Linux Yocto, other Linux distributions upon request	
	Device Management	BlueSphere MDM (Android version optional)	
Mechanical	Dimensions	82mm × 50mm × 4.71mm (SMARC 2.1form factor)	
Environmental Condition	Temperature	Operating: -20℃ ~ +70℃ (Optional: -40℃~+85℃)	Storage: -40℃ ~ +85℃
	Humidity	5%~95% RH (Non-condensing)	

I/O		
Display (Support two independent displays)	2 × 4-lane LVDS / 1 × 8-lane LVDS, up to 1920 × 1080@60Hz	
	1 × HDMI 2.0, up to 4K @30Hz or 3840 × 1440 @60Hz	
Camera	1 × 2-lane MIPI CSI	
	1 × 4-lane MIPI CSI	
Audio	2 × I^2S	
Ethernet	2 × 10/100/1000 Base-T	
Wi-Fi & Bluetooth	Wi-Fi IEEE 802.11 a/b/g/n/ac + BT5.2	
USB	1 × USB 2.0 OTG	4 × USB 2.0 host
	2 × USB 3.0 Host	
UART	3 × Communication UART (1.8V)	1 × Debug UART (1.8V)
CAN	2 × CAN	
GPIO	14 × GPIO (Max.)	
SPI	2 × SPI	
I^2C	4 × I^2C	
PCIe	2 × PCIe 3.0 x1	
SDIO	1 × 4-bit SDIO 3.0	
ADC	4 × ADC input	
Button signal	1 × Power button	1 × Reset button
	1 × Recovery button	2 × Boot mode switch
Watchdog Timer	Supported (On-chip watchdog, discrete watchdog)	
RTC	Supported	

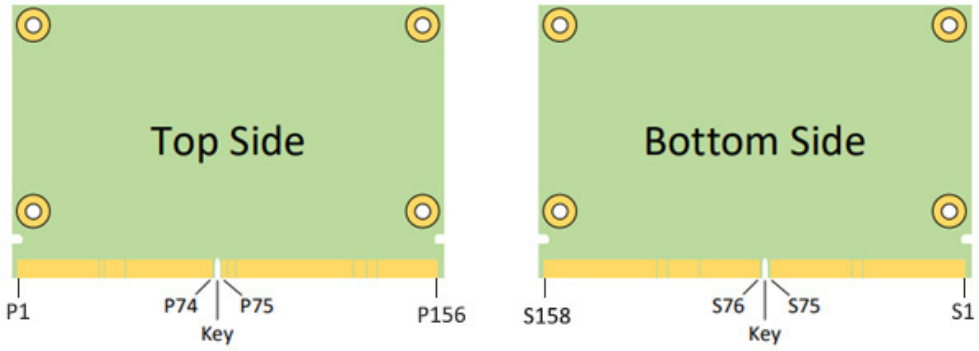
Product Outlines



Block Diagram



Edge Connector Pinout



Primary (Top) Side	Function	I/O Type	Level	Description
P1	NC	-	-	No connection
P2	GND	P	-	Ground
P3	CSI2_CKP	O	-	MIPI CSI 2 differential clock +
P4	CSI2_CKN	O	-	MIPI CSI 2 differential clock -
P5	NC	-	-	No connection
P6	NC	-	-	No connection
P7	CSI2_DP0	I	-	MIPI CSI 2 receive data 0 +
P8	CSI2_DN0	I	-	MIPI CSI 2 receive data 0 -
P9	GND	P	-	Ground
P10	CSI2_DP1	I	-	MIPI CSI 2 receive data 1 +
P11	CSI2_DN1	I	-	MIPI CSI 2 receive data 1 -
P12	GND	P	-	Ground
P13	CSI2_DP2	I	-	MIPI CSI 2 receive data 2 +
P14	CSI2_DN2	I	-	MIPI CSI 2 receive data 2 -
P15	GND	P	-	Ground
P16	CSI2_DP3	I	-	MIPI CSI 2 receive data 3 +
P17	CSI2_DN3	I	-	MIPI CSI 2 receive data 3 -
P18	GND	P	-	Ground
P19	ENET0_PHY_TRXN3	I/O	-	GBE 0 transmit/receive pair 3 -
P20	ENET0_PHY_TRXP3	I/O	-	GBE 0 transmit/receive pair 3 +
P21	LED0_LINK10_100	O	-	GBE 0 10_100 Mbps link indicator, active high
P22	LED0_LINK1000	O	-	GBE 0 1000Mbps link indicator, active high

Primary (Top) Side	Function	I/O Type	Level	Description
P23	ENET0_PHY_TRXN2	I/O	-	GBE 0 transmit/receive pair 2 -
P24	ENET0_PHY_TRXP2	I/O	-	GBE 0 transmit/receive pair 2 +
P25	LED0_ACT	O	3.3V (PU)	GBE 0 activity indicator, active low
P26	ENET0_PHY_TRXN1	I/O	-	GBE 0 transmit/receive pair 1 -
P27	ENET0_PHY_TRXP1	I/O	-	GBE 0 transmit/receive pair 1 +
P28	GBE0_CTREF	P	-	Center-tap reference voltage for carrier board Ethernet magnetics (if required by the GBE PHY module)
P29	ENET0_PHY_TRXN0	I/O	-	GBE 0 transmit/receive pair 0-
P30	ENET0_PHY_TRXP0	I/O	-	GBE 0 transmit/receive pair 0+
P31	NC	-	-	No connection
P32	GND	P	-	Ground
P33	SD2_WP	O	1.8V	SDIO write protect
P34	SD2_CMD	O	1.8V	SDMMC 0 command/response
P35	SD2_nCD	I	1.8V	SDMMC 0 card detection
P36	SD2_CLK	O	1.8V	SDMMC 0 clock
P37	SD2_PWR_EN	O	3.3V (PU)	SDMMC 0 power enable
P38	GND	P	-	Ground
P39	SD2_DATA0	I/O	3.3V/1.8V	SDMMC 0 data 0
P40	SD2_DATA1	I/O	3.3V/1.8V	SDMMC 0 data 1
P41	SD2_DATA2	I/O	3.3V/1.8V	SDMMC 0 data 2
P42	SD2_DATA3	I/O	3.3V/1.8V	SDMMC 0 data 3
P43	ECSPI1_SS0	O	1.8V	SPI 1 chip select
P44	ECSPI1_SCLK	O	1.8V	SPI 1 clock
P45	ECSPI1_MISO	I	1.8V	SPI 1 master in slave out
P46	ECSPI1_MOSI	O	1.8V	SPI 1 master out slave in
P47	GND	P	-	Ground
P48	NC	-	-	No connection
P49	NC	-	-	No connection
P50	GND	P	-	Ground
P51	NC	-	-	No connection
P52	NC	-	-	No connection
P53	GND	P	-	Ground
P54	ECSPI2_SS0	O	1.8V	SPI 2 chip select 0
P55	NC	-	-	No connection
P56	ECSPI2_SCLK	O	1.8V	SPI 2 clock

Primary (Top) Side	Function	I/O Type	Level	Description
P57	ECSPI2_MISO	I	1.8V	SPI 2 master in slave out
P58	ECSPI2_MOSI	O	1.8V	SPI 2 master out slave in
P59	GND	P	-	Ground
P60	USB0_DP	I/O	-	USB 2.0 port 0 OTG data +
P61	USB0_DN	I/O	-	USB 2.0 port 0 OTG data -
P62	NC	-	-	No connection
P63	USB0_VBUS_DET	I	5V	USB 2.0 port 0 power detection
P64	USB0_ID	I	3.3V (PD)	USB 2.0 port 0 OTG identification (pull to ground to indicate device insertion, pull to 3.3V within 1KΩ to indicate host insertion)
P65	USB_DP_DN1	I/O	-	USB 2.0 port 1 data +
P66	USB_DM_DN1	I/O	-	USB 2.0 port 1 data -
P67	NC	-	-	No connection
P68	GND	P	-	Ground
P69	USB_DP_DN2	I/O	-	USB 2.0 port 2 data +
P70	USB_DM_DN2	I/O	-	USB 2.0 port 2 data -
P71	NC	-	-	No connection
P72	NC	-	-	No connection
P73	NC	-	-	No connection
P74	NC	-	-	No connection
		KEY		
P75	PCIE_RST	O	3.3V (PU)	PCIe 3.0 port A reset, active low
P76	NC	I	3.3V	No connection
P77	NC	I	3.3V	No connection
P78	PCIE_CKREQ#	I	3.3V	PCIe 3.0 port A clock request, active low
P79	GND	P	-	Ground
P80	NC	-	-	No connection
P81	NC	-	-	No connection
P82	GND	P	-	Ground
P83	PCIE_CLKP	O	-	PCIe 3.0 port A reference clock +
P84	PCIE_CLKN	O	-	PCIe 3.0 port A reference clock -
P85	GND	P	-	Ground
P86	PCIE_RXP	I	-	PCIe 3.0 port A receiver +
P87	PCIE_RXN	I	-	PCIe 3.0 port A receiver -
P88	GND	P	-	Ground
P89	PCIE_TXP	I	-	PCIe 3.0 port A transmitter +
P90	PCIE_TXN	I	-	PCIe 3.0 port A transmitter -
P91	GND	P	-	Ground
P92	HDMI_TXP2	O	-	HDMI transmit data 2 +
P93	HDMI_TXN2	O	-	HDMI transmit data 2 -

Primary (Top) Side	Function	I/O Type	Level	Description
P94	GND	P	-	Ground
P95	HDMI_TXP1	O	-	HDMI 2.0 transmit data 1 +
P96	HDMI_TXN1	O	-	HDMI 2.0 transmit data 1 -
P97	GND	P	-	Ground
P98	HDMI_TXP0	O	-	HDMI 2.0 transmit data 0 +
P99	HDMI_TXN0	O	-	HDMI 2.0 transmit data 0 -
P100	GND	P	-	Ground
P101	HDMI_TXCP	O	-	HDMI 2.0 transmit data 3 +
P102	HDMI_TXCN	O	-	HDMI 2.0 transmit data 3 -
P103	GND	P	-	Ground
P104	HDMI_HPD	I	2.5~5V (PD)	HDMI 2.0 hot plug detection input
P105	HDMI_DDC_SCL	O	1.8V (PU)	I2C_CLK line dedicated to HDMI
P106	HDMI_DDC_SDA	I/O	1.8V (PU)	I2C_DAT line dedicated to HDMI
P107	NC	-	-	No connection
P108	GPIO0/CAM0_PWR#	I/O	1.8V	Default: CAM 0 power enable
P109	GPIO1/CAM1_PWR#	I/O	1.8V	Default: CAM 1 power enable
P110	GPIO2/CAM0_RST#	I/O	1.8V	Default: CAM 0 reset
P111	GPIO3/CAM1_RST#	I/O	1.8V	Default: CAM 1 reset
P112	GIPO4/HDA_RST#	I/O	1.8V	GPIO
P113	GIPO5/PWM	I/O	1.8V	GPIO
P114	GIPO6/TACHIN	I/O	1.8V	GPIO
P115	GIPO7	I/O	1.8V	GPIO
P116	GPIO8	I/O	1.8V	GPIO
P117	GPIO9	I/O	1.8V	GPIO
P118	GPIO10	I/O	1.8V	GPIO
P119	GPIO11	I/O	1.8V	GPIO
P120	GND	P	-	Ground
P121	I2C1_PM_CL	O	1.8V (PU)	I^2C clock for power management
P122	I2C1_PM_DATA	I/O	1.8V (PU)	I^2C data for power management
P123	BOOT_SEL0#	-	-	Boot mode select 0
P124	BOOT_SEL1#	-	-	Boot mode select 1
P125	NC	-	-	No connection
P126	RESET_OUT#	O	1.8V (PU)	Peripheral reset (module to board), active low
P127	RESET_IN#	I	1.8V (PU)	Module reset (board to module), active low
P128	POWER_BTN#	I	1.8V (PU)	Power button (pull low for 10ms to trigger). The board is set to power up via VDD_IN supply without power button trigger.
P129	UART1_TX	O	1.8V	Debug UART transmit data
P130	UART1_RX	I	1.8V (PU)	Debug UART receive data

Primary (Top) Side	Function	I/O Type	Level	Description
P131	UART1_RTS	O	1.8V	Debug UART request to send
P132	UART1_CTS	I	1.8V (PU)	Debug UART clear to send
P133	GND	P	-	Ground
P134	UART2_TX	O	1.8V (PD)	UART 2 transmit data
P135	UART2_RX	I	1.8V (PU)	UART 2 receive data
P136	NC	-	-	No connection
P137	NC	-	-	No connection
P138	NC	-	-	No connection
P139	NC	-	-	No connection
P140	UART4_TX	O	1.8V	UART 4 transmit data
P141	UART4_RX	I	1.8V	UART 4 receive data
P142	GND	P	0V	Ground
P143	FLEXCAN1_TX	O	1.8V	CAN 1 transmit data
P144	FLEXCAN1_RX	I	1.8V	CAN 1 receive data
P145	FLEXCAN2_TX	O	1.8V	CAN 2 transmit data
P146	FLEXCAN2_RX	I	1.8V	CAN 2 receive data
P147	VDD_IN	P	5.0V	5V power input
P148	VDD_IN	P	5.0V	5V power input
P149	VDD_IN	P	5.0V	5V power input
P150	VDD_IN	P	5.0V	5V power input
P151	VDD_IN	P	5.0V	5V power input
P152	VDD_IN	P	5.0V	5V power input
P153	VDD_IN	P	5.0V	5V power input
P154	VDD_IN	P	5.0V	5V power input
P155	VDD_IN	P	5.0V	5V power input
P156	VDD_IN	P	5.0V	5V power input

Secondary (Bottom) Side	Function	I/O Type	Level	Description
S1	I2C3_CAM1_CK	O	1.8V (PU)	I^2C clock for CAM 1
S2	I2C3_CAM1_DAT	I/O	1.8V (PU)	I^2C data for CAM 1
S3	GND	P	-	Ground
S4	I2S2_MCLK	-	-	I^2S 2 master clock
S5	I2C3_CAM0_CK	O	1.8V (PU)	I^2C clock for CAM 0
S6	CAM_MCK	O	1.8V	Master clock for CAM 0&1
S7	I2C3_CAM0_DAT	I/O	1.8V (PU)	I^2C data for CAM 0
S8	CSI1_CKP	O	-	MIPI CSI 1 (CAM0) differential clock +
S9	CSI1_CKN	O	-	MIPI CSI 1 (CAM0) differential clock -
S10	GND	P	-	Ground

Secondary (Bottom) Side	Function	I/O Type	Level	Description
S11	CSI1_DP0	I	-	MIPI CSI 1 (CAM0) receive data 0 +
S12	CSI1_DN0	I	-	MIPI CSI 1 (CAM0) receive data 0 -
S13	GND	P	-	Ground
S14	CSI1_DP1	I	-	MIPI CSI 1 (CAM0) receive data 1 +
S15	CSI1_DN1	I	-	MIPI CSI 1 (CAM0) receive data 1 -
S16	GND	P	-	Ground
S17	ENET1_PHY_TRXP0	I/O	-	GBE 1 transmit/receive pair 0 -
S18	ENET1_PHY_TRXN0	I/O	-	GBE 1 transmit/receive pair 0 +
S19	ENET1_LED_LINK10_100	O	-	GBE 1 10_100 Mbps link indicator, active high
S20	ENET1_PHY_TRXP1	I/O	-	GBE 1 transmit/receive pair 1 -
S21	ENET1_PHY_TRXN1	I/O	-	GBE 1 transmit/receive pair 1 +
S22	ENET1_LED_LINK1000	O	3.3V (PD)	GBE 1 1000 Mbps link indicator
S23	ENET1_PHY_TRXP2	I/O	-	GBE 1 transmit/receive pair 2 -
S24	ENET1_PHY_TRXN2	I/O	-	GBE 1 transmit/receive pair 2 +
S25	GND	P	-	Ground
S26	ENET1_PHY_TRXP3	I/O	-	GBE 1 transmit/receive pair 3 -
S27	ENET1_PHY_TRXN3	I/O	-	GBE 1 transmit/receive pair 3 +
S28	GBE1_CTREF	P	-	Center-tap reference voltage for carrier board Ethernet magnetics (if required by the GBE PHY module)
S29	NC	-	-	No connection
S30	NC	-	-	No connection
S31	LED_ACT1	O	3.3V	GBE 1 activity indicator, active low
S32	NC	-	-	No connection
S33	NC	-	-	No connection
S34	GND	P	-	Ground
S35	USB_DP_DN4	I/O	-	USB 2.0 port 4 data +
S36	USB_DM_DN4	I/O	-	USB 2.0 port 4 data -
S37	NC	-	-	No connection
S38	AUDIO_MCK	O	1.8V	I^2S 0&1 master clock
S39	I2S0_LRCK	I/O	1.8V	I^2S 0 left/right clock
S40	I2S0_SDOUT	O	1.8V	I^2S 0 serial data output
S41	I2S0_SDIN	DI	1.8V	I^2S 0 serial data input
S42	I2S0_CK	O	1.8V	I^2S 0&1 bit clock
S43	NC	-	-	No connection
S44	NC	-	-	No connection
S45	NC	-	-	No connection

Secondary (Bottom) Side	Function	I/O Type	Level	Description
S46	NC	-	-	No connection
S47	GND	P	-	Ground
S48	I2C_GP_CLK	O	1.8V (PU)	I^2C general purpose clock
S49	I2C_GP_SDA	I/O	1.8V (PU)	I^2C general purpose data
S50	I2S2_LRCK	-	-	I^2S 2 left/right clock
S51	I2S2_SDOUT	-	-	I^2S 2 serial data output
S52	I2S2_SDIN	-	-	I^2S 2 serial data input
S53	I2S2_CK	-	-	I^2S 2 serial clock
S54	NC	-	-	No connection
S55	NC	-	-	No connection
S56	NC	-	-	No connection
S57	NC	-	-	No connection
S58	NC	-	-	No connection
S59	NC	-	-	No connection
S60	NC	-	-	No connection
S61	GND	P	-	Ground
S62	USB_SSTXP_DN3	O	-	USB 3.0 port 3 data output + (host only)
S63	USB_SSTXM_DN3	O	-	USB 3.0 port 3 data output - (host only)
S64	GND	P	-	Ground
S65	USB_SSRXP_DN3	I	-	USB 3.0 port 3 data input + (host only)
S66	USB_SSRXM_DN3	I	-	USB 3.0 port 3 data input - (host only)
S67	GND	P	-	Ground
S68	USB_DP_DN3	I/O	-	USB 2.0 port 3 data + (host only)
S69	USB_DM_DN3	I/O	-	USB 2.0 port 3 data - (host only)
S70	GND	P	-	Ground
S71	USB_SSTXP_DN2	-	-	USB 3.0 port 2 data output + (host only)
S72	USB_SSTXM_DN2	-	-	USB 3.0 port 2 data output - (host only)
S73	GND	P	-	Ground
S74	USB_SSRXP_DN2	-	-	USB 3.0 port 2 data input + (host only)
S75	USB_SSRXM_DN2	-	-	USB 3.0 port 2 data input - (host only)
KEY				
S76	NC	-	-	No connection
S77	NC	-	-	No connection
S78	NC	-	-	No connection
S79	NC	-	-	No connection
S80	GND	-	-	Ground
S81	NC	-	-	No connection
S82	NC	-	-	No connection
S83	GND	-	-	Ground

Secondary (Bottom) Side	Function	I/O Type	Level	Description
S84	NC	-	-	No connection
S85	NC	-	-	No connection
S86	GND	P	-	Ground
S87	NC	-	-	No connection
S88	NC	-	-	No connection
S89	GND	P	0V	Ground
S90	NC	-	-	No connection
S91	NC	-	-	No connection
S92	GND	P	-	Ground
S93	NC	-	-	No connection
S94	NC	-	-	No connection
S95	NC	-	-	No connection
S96	NC	-	-	No connection
S97	NC	-	-	No connection
S98	NC	-	-	No connection
S99	NC	-	-	No connection
S100	NC	-	-	No connection
S101	GND	P	-	Ground
S102	NC	-	-	No connection
S103	NC	-	-	No connection
S104	NC	-	-	No connection
S105	NC	-	-	No connection
S106	NC	-	-	No connection
S107	LCD1_BKLT_EN	O	1.8V	LCD 1 backlight enable
S108	LVDS1_CLK_P	O	-	LVDS 1 clock +
S109	LVDS1_CLK_N	O	-	LVDS 1 clock -
S110	GND	P	-	Ground
S111	LVDS1_TX0_P	O	-	LVDS 1 data 0 +
S112	LVDS1_TX0_N	O	-	LVDS 1 data 0 -
S113	NC	-	-	No connection
S114	LVDS1_TX1_P	O	-	LVDS 1 data 1 +
S115	LVDS1_TX1_N	O	-	LVDS 1 data 1 -
S116	LCD1_VDD_EN	O	1.8V	LVDS VDD enable
S117	LVDS1_TX2_P	O	-	LVDS 1 data 2 +
S118	LVDS1_TX2_N	O	-	LVDS 1 data 2 -
S119	GND	P	-	Ground
S120	LVDS1_TX3_P	O	-	LVDS 1 data 3 +
S121	LVDS1_TX3_N	O	-	LVDS 1 data 3 -

Secondary (Bottom) Side	Function	I/O Type	Level	Description
S122	LCD1_BKLT_PWM	O	1.8V	LCD 1 backlight PWM control
S123	GPIO13	I/O	1.8V	GPIO
S124	GND	P	-	Ground
S125	LVDS0_TX0_P	O	-	LVDS 0 data 0 +
S126	LVDS0_TX0_N	O	-	LVDS 0 data 0 -
S127	LCD0_BKLT_EN	O	1.8V	LCD 0 backlight enable
S128	LVDS0_TX1_P	O	-	LVDS 0 data 1 +
S129	LVDS0_TX1_N	O	-	LVDS 0 data 1 -
S130	GND	P	-	Ground
S131	LVDS0_TX2_P	O	-	LVDS 0 data 2 +
S132	LVDS0_TX2_N	O	-	LVDS 0 data 2 -
S133	LCD0_VDD_EN	O	1.8V	LCD 0 VDD enable
S134	LVDS0_CLK_P	O	-	LVDS 0 clock +
S135	LVDS0_CLK_N	O	-	LVDS 0 clock -
S136	GND	P	-	Ground
S137	LVDS0_TX3_P	O	-	LVDS 0 data 3 +
S138	LVDS0_TX3_N	O	-	LVDS 0 data 3 -
S139	I2C2_LCD_CK	O	1.8V (PU)	I^2C clock for LCD 0&1
S140	I2C2_LCD_DAT	I/O	1.8V (PU)	I^2C data for LCD 0&1
S141	LCD0_BKLT_PWM	O	1.8V	LCD 0 backlight PWM control
S142	GPIO12	I/O	1.8V	GPIO
S143	GND	P	-	Ground
S144	NC	-	-	No connection
S145	WDT_TIME_OUT	O	1.8V	Watchdog timeout configuration
S146	NC	-	-	No connection
S147	RTC_VBAT	P	3V	RTC battery power supply
S148	LID#	-	-	Lid open/close indication to module, active low

Secondary (Bottom) Side	Function	I/O Type	Level	Description
S149	SLEEP#	-	-	Sleep indicator from the carrier board. May be sourced from user Sleep button or Carrier logic.
S150	VIN_PWR_BAD#	-	-	Main input power fault alarm signal
S151	CHARGING#	-	-	Charger presence detect signal
S152	CHARGER_PRSNT#	-	-	Charging status indication signal
S153	CARRIER_STBY#	O	1.8V	The module shall drive this signal low when the system is in a standby power state.
S154	CARRIER_PWR_ON	O	1.8V	Carrier board circuits (apart from power management and power path circuits) should not be powered up until the module asserts the CARRIER_PWR_ON signal.
S155	NC	-	-	No connection
S156	BATLOW#	-	-	Battery low voltage detect input
S157	TEST#	-	-	Factory test mode control
S158	GND	-	-	Ground

Ordering Information

Ordering No.	Operating System	Memory	Storage	Form factor
VT-SBC-SMARC-8MP-A	Android	2GB	16GB	SMARC 2.1
VT-SBC-SMARC-8MP-Y	Yocto	2GB	16GB	SMARC 2.1

Packing List	
VT-SBC-SMARC-8MP computer-on-module	1