VT-SBC-3568 Single Board Computer



User Manual

Version: 1.1

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Revision History

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Table of Contents

Foreword	Ι	1			
CHAPTER	1 INTRODUCTION	5			
1.1	Product Overview	6			
1.2	Terminology	6			
1.3	Block Diagram	7			
1.4	Specifications	8			
1.5	Operating system	9			
1.6	Mechanical Dimensions	9			
1.7	Power Supply and Consumption	9			
1.8	Environmental Specifications	9			
CHAPTER	2 CONNECTOR DESCRIPTION	. 10			
2.1	SBC Layout	11			
2.2	Memory and Storage	.11			
2.2.1	LPDDR4 RAM	11			
2.2.2	eMMC Flash	11			
2.2.3	EEPROM	11			
2.3	Identification of Pin 1	. 12			
2.4	Connector	12			
2.4.1	Power input (1)	. 12			
2.4.2	J23 Ethernet port (2)	13			
2.4.3	J13 Wi-Fi and Bluetooth (3)	13			
2.4.4	J9 HDMI (4)	13			
2.4.5	J11 LVDS (5)	13			
2.4.6	J12 Backlight connector (6)	.14			
2.4.7	J10 MIPI DSI (7)	15			
2.4.8	J8 MIPI CSI (8)	16			
2.4.9	J22 I ² C (9)	17			
2.4.10	U40 USB 2.0 Type-A (10)	. 18			
2.4.11	J3/J4/J5 USB 2.0 (11)	. 18			
2.4.12	J6 USB 3.0 (12)	. 18			
2.4.13	J7 USB 2.0 Type-C (13)	.19			
2.4.14	J21 GPIO (14)	. 19			
2.4.15	J20 40-pin header (15)	20			
2.4.16	J17 M.2 Key B slot (16)	21			
2.4.17	J16 Audio jack (17)	.22			
2.4.18	J15 Speaker connector (18)	.23			
2.4.19	Buttons (19-21)	23			
2.4.20	J18 Micro SIM (22)	.23			
CHAPTER	CHAPTER 3 SOFTWARE				
3.1	Prerequisites	.25			
3.2	Firmware Upgrade in Loader Mode (Windows system)	. 25			
3.2.1	Driver installation	.25			

3.2.2	ADB setup	26
3.2.3	Image download	27
3.3	Firmware Upgrade in Loader Mode (Ubuntu system)	29
3.3.1	ADB setup	29
3.3.2	Image Download	30
3.4	Development Guide	30
3.4.1	Static IP setup	30
3.4.2	Qt setup for cross compilation	31
3.5	Use MySQL	34
3.6	Interface Configuration	34
3.6.1	Serial port	34
3.6.2	CAN	34
3.6.3	GPIO	35
3.6.4	Ethernet	36
CHAPTER	4 DISPOSAL AND WARRANTY	37
4.1	Disposal	38
4.2	Warranty	39

Foreword

Thank you for purchasing VT-SBC-3568 single board computer ("the Board" or "the Product"). This manual intends to provide guidance and assistance necessary on setting up, operating or maintaining the Product. Please read this manual and make sure you understand the functionality of the Product before putting it into use.

Intended Users

This manual is intended for:

- Network architects/programmers
- Other technically qualified personnel

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It is our practice to change part numbers when published ratings or features are changed, or when significant construction changes are made. However, some specifications of the Product may be changed without notice.

Technical Support and Assistance

Should you have any question about the Product that is not covered in this manual, contact your sales representative for solution. Please include the following information in your question:

- Product name and PO number;
- Complete description of the problem;
- Error message you received, if any.

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Symbology

This manual uses the following signs to prompt users to pay special attention to relevant information.

Â	Caution for latent damage to system or harm to personnel
ì	Attention to important information or regulations

General Safety Instructions

The Product is supposed be installed by knowledgeable, skilled persons familiar with local and/or international electrical codes and regulations. For your safety and prevention of damage to the Product, please read and observe carefully the following safety instructions prior to installation and operation. Keep this manual well for future reference.

- Do not disassemble or otherwise modify the Product. Such action may cause heat generation, ignition, electronic shock, or other damages including human injury, and may void your warranty.
- Keep the Product away from heat source, such as heater, heat dissipater, or engine casing.
- Do not insert foreign materials into any opening of the Product as it may cause the Product to malfunction or burn out.
- To ensure proper functioning and prevent overheating of the Product, do not cover or block the ventilation holes of the Product.
- Follow the installation instructions with the installation tools provided or recommended.
- The use or placement of the operation tools shall comply with the code of practice of such tools to avoid short circuit of the Product.
- Cut off the power before inspection of the Product to avoid human injury or product damage.

Precautions for Power Cables and Accessories

- Use proper power source only. Make sure the supply voltage falls within the specified range.
- Place the cables properly at places without extrusion hazards.
- There is a coin cell battery for powering the RTC. Therefore, please avoid short circuit of the battery during transportation or operation at high temperatures.
- Cleaning instructions:
 - Power off before cleaning the Product
 - Do not use spray detergent
 - Clean with a damp cloth
 - Do not try to clean exposed electronic components unless with a dust collector

Power off and contact Vantron technical support engineer in case of the following faults:

- The Product is damaged
- The temperature is excessively high
- Fault is still not solved after troubleshooting according to this manual

Do not use in combustible and explosive environment:

- Keep away from combustible and explosive environment
- Keep away from all energized circuits
- Unauthorized removal of the enclosure from the device is not allowed. Do not change components unless the power cable is unplugged. In some cases, the device may still have residual voltage even if the power cable is unplugged. Therefore, it is a must to remove and fully discharge the device before replacement of the components.

CHAPTER 1



1.1 Product Overview

VT-SBC-3568 single board computer is based on Rockchip RK3568 processor that integrates quad-core ARM Cortex-A55 CPU and Mali G52 GPU to provide optimized performance at lower power consumption, and offer high-quality video encoding and decoding for better display performance.

While both wired and wireless network accesses are available, user data is kept safe and secure in transmission. Meanwhile, VT-SBC-3568 provides a range of customer expansion options to meet the requirements of different application purposes, especially in industrial IoT scenarios.

Featuring high flexibility and high performance, VT-SBC-3568 could work under extreme environments at industrial-grade temperatures ranging from 0° C to +60°C, making it a reliable industrial IoT solution.

Terminology	Description		
NC	No connection		
VCC	Voltage common collector		
GND	Ground		
/	Active low signal		
+	Positive of difference signal		
-	Negative of difference signal		
1	Input		
0	Output		
I/O	Input/output		
Р	Power or ground		
А	Analog		
OD	Open drain		
CMOS	3.3 V CMOS		
LVCMOS	Low Voltage CMOS		
LVTTL	Low Voltage TTL		
3.3V	3.3 V signal level		
5V	5V signal level		
USB	5V tolerant signal		
PCIe	PCI Express signal, not 3.3 V tolerant		
MMC	Multimedia Card		

1.2 Terminology

1.3 Block Diagram



1.4 Specifications

VT-SBC-3568				
	CPU	RK3568, Quad-core ARM Cortex-A55 MPCore, up to 2.0 GHz		
	GPU	ARM Mali-G52		
System	Memory	4GB LPDDR4 (Optional: 2GB)		
	Storage	16GB eMMC V5.1, up to 128GB 2Kb EEPROM		
	Ethernet	1 x RJ45, 10/100/1000Mbps 100Base-T4 (surge suppression)		
Communication	Cellular	4G/5G (Optional)		
	Wi-Fi & BT	802.11 a/b/g/n/ac & BT 5.0		
	Display	Dual LVDS, Resolution up to 1920 x 1080, with backlight connector (Optional: eDP, Resolution up to 2K)		
	тр	Infrared touch papel (Ontional)		
Media	номі			
	Camera	1 x 4-lane MIPI CSI		
	Cumera	1 x 3 5mm Audio jack		
	Audio	1 x Speaker, up to 15W		
	Carial	5 x UART (RS232 & RS485)		
	Serial	1 x UART for debug		
1/Os	USB	1 x USB 3.0 3 x USB 2.0 1 x USB 2.0 Type-C		
., 00	GPIO	24 x GPIO		
	SIM slot	1 x Micro SIM slot		
	RTC	Supported		
	WDT	Supported		
Expansion	Bus	2 x CAN, without transmitter 1 x I ² C for TP 1 x USB 2.0 host for TP 1 x M.2 key B for 4G/5G module and SSD		
System Control	Button	1 x Reset 1 x Power button 1 x Volume button		
Power	Input	12V/3A DC		
Software	Operating system	Debian 10, Android 11+		
Mechanical	Dimensions	146.02 x 102mm		
witchiallical	Cooling mode	Fanless		
Environment	Temperature	Operating: 0°C~+60°C (RK3568), -20°C~+70°C (RK3568J) Storage: -20°C~+70°C (RK3568), -40°C~+85°C (RK3568J)		
condition	Humidity	≤96%RH (Non-condensing)		

1.5 Operating system

VT-SBC-3568 supports Debian 10 and Android 11+ operating systems.

1.6 Mechanical Dimensions

• 146.02mm x 102mm





1.7 Power Supply and Consumption

VT-SBC-3568 works with 12V/3A DC power supply.

The power consumption of the Board is 30W at the maximum. It should be pointed out that power consumption is largely determined by the RAM, storage capacity, and other configurations of the board.

1.8 Environmental Specifications

VT-SBC-3568 works at a temperature ranging from $0^{\circ}C$ to $+60^{\circ}C$ (for 3568J: $-20^{\circ}C$ to $+70^{\circ}C$) and is designed to be stored at a temperature ranging from $-20^{\circ}C$ to $+70^{\circ}C$ (for 3568J: $-40^{\circ}C^{\circ}+85^{\circ}C$) and a humidity of no more than 96% RH for non-condensing purpose.

CHAPTER 2

CONNECTOR DESCRIPTION

2.1 SBC Layout



2.2 Memory and Storage

2.2.1 LPDDR4 RAM

VT-SBC-3568 is equipped with a 4GB LPDDR4 RAM by default, and users also have the option of 2GB RAM.

2.2.2 eMMC Flash

VT-SBC-3568 provides an eMMC 5.1 flash up to 128 GB, and the default capacity is 16GB. It is used as the default boot and storage device.

2.2.3 EEPROM

VT-SBC-3568 provides a 2Kb EEPROM to store hardware configuration information.

2.3 Identification of Pin 1

Unless otherwise stated, pin 1 of a connector is seated on a square pad that is different from the round pads used for other pins. Sometimes, pin 1 is next to a trigonal mark on the board. When there are two rows of pins on a connector, the row with pin 1 is composed of odd numbers and the other is composed of even numbers.





Usually, there will be numbers or marks next to the pins of a connector on the board to indicate the pinouts.



2.4 Connector

This section is going to brief the connectors/jumpers on the Board with corresponsive pinout description.

2.4.1 Power input (1)

VT-SBC-3568 provides a 4-pin power connector and a female power jack to supply power for the Board.





Pinout description of the 4-pin power connector:

Pin	Name	Туре	Description
1	+VDC	Р	DC-IN POWER +
2	+VDC	Р	DC-IN POWER +
3	-VDC	Р	DC-IN POWER -
4	-VDC	Р	DC-IN POWER -

2.4.2 J23 Ethernet port (2)

VT-SBC-3568 offers an RJ45 ethernet jack with two LEDs, green for activity indication and yellow for link indication. The interface supports 10/100/1000 Mbps transmission rate.

2.4.3 J13 Wi-Fi and Bluetooth (3)

VT-SBC-3568 offers an AP6256 1T1R combo SiP module, combining 802.11 a/b/g/n/ac Wi-Fi and Bluetooth 5.0.

2.4.4 J9 HDMI (4)

VT-SBC-3568 offers a standard HDMI Type-A interface for image output. The pinout description of the interface is in line with the pin assignment of standard HDMI Type-A interface.

2.4.5 J11 LVDS (5)

VT-SBC-3568 offers a dual LVDS interface to connect high-definition displays (resolution up to 1920 x 1080).

Specifications: 1 x 40, 0.5mm, 0.5A, 1.00mm (H), male, RA, SMT, RoHS (IPEX: 20455-040E66).



Pin	Name	Туре	Description
1	PANEL_BL_PWM	0	LCD backlight PWM control output
2	PANEL_BKLTEN	0	LCD backlight power control output
3	LVDS_B_D3+	0	LVDS B Lane3 +
4	LVDS_B_D3-	0	LVDS B Lane3 -
5	LVDS_B_CLK+	0	LVDS B CLK+
6	LVDS_B_CLK-	0	LVDS B CLK-
7	NC		
8	LVDS_B_D2+	0	LVDS B Lane2 +
9	LVDS_B_D2-	0	LVDS B Lane2 -
10	LVDS_B_D1+	0	LVDS B Lane1 +
11	LVDS_B_D1-	0	LVDS B Lane1 -
12	SEL68	0	
13	NC		

14	eDP_HPD	I	热插拔检测信号
15	LVDS_B_D0+	0	LVDS B Lane0 +
16	LVDS_B_D0-	0	LVDS B Lane0 -
17	LVDS_DDC_DATA	I/O	I2C_SDA
18	LVDS_DDC_CLK	0	I2C_SCL
19	GND	Р	接地
20	LVDS_A_D3+	0	LVDS A Lane3 +
21	LVDS_A_D3-	0	LVDS A Lane3 -
22	GND	Р	接地
23	LVDS_A_CLK+	0	LVDS A CLK +
24	LVDS_A_CLK-	0	LVDS A CLK -
25	GND	Р	接地
26	LVDS_A_D2+	0	LVDS A Lane2 +
27	LVDS_A_D2-	0	LVDS A Lane2 -
28	GND	Р	接地
29	LVDS_A_D1+	0	LVDS A Lane1 +
30	LVDS_A_D1-	0	LVDS A Lane1 -
31	GND	Р	接地
32	LVDS_A_D0+	0	LVDS A Lane0 +
33	LVDS_A_D0-	0	LVDS A Lane0 -
34	GND	Р	接地
35	NC		
36	NC		
37	LCD_VDD	Р	LCD 电源
38	LCD_VDD	Р	LCD 电源
39	LCD_VDD	Р	LCD 电源
40	LCD_VDD	Р	LCD 电源

2.4.6 J12 Backlight connector (6)

VT-SBC-3568 offers a backlight connector that supplies power for the LCD panel.

Specifications: 1 x 6, 2.0mm, 2A, 6mm (H), male, RA, -25°C \sim 85°C, THR, RoHS (JST: B6B-PH-K-S (LF) (SN)



Pinout description:

Pin	Name	Туре	Description
1	LCD_BLK	Р	Power supply 12V for backlight
2	LCD_BLK	Р	Power supply 12V for backlight
3	PANEL_BKLTEN	0	LCD backlight power control output
4	PANEL_BL_PWM	0	LCD backlight PWM control output
5	GND	Р	Ground
6	GND	Р	Ground

2.4.7 J10 MIPI DSI (7)

VT-SBC-3568 offers a 4-lane MIPI DSI connector for connecting displays.

Specifications: 1 x 31, 0.3mm, 1.0mm(H), RA, Bot, WDT, SMT, RoHS (LZR: FPC031003-31LG)



Pin	Name	Туре	Description
1	LED+	Р	Power supply for backlight
2	LED+	Р	Power supply for backlight
3	LED+	Р	Power supply for backlight
4	NC		
5	LED-	Р	Feedback for current of backlight
6	LED-	Р	Feedback for current of backlight
7	LED-	Р	Feedback for current of backlight
8	LED-	Р	Feedback for current of backlight
9	GND	Р	Ground
10	GND	Р	Ground
11	MIPI_D2P	0	MIPI DSI Lane2 +
12	MIPI_D2N	0	MIPI DSI Lane2 -
13	GND	Р	Ground
14	MIPI_D1P	0	MIPI DSI Lane1 +
15	MIPI_D1N	0	MIPI DSI Lane1 -
16	GND	Р	Ground
17	MIPI_CLK+	0	MIPI DSI CLK+
18	MIPI_CLK-	0	MIPI DSI CLK-
19	GND	Р	Ground
20	MIPI_D0P	0	MIPI DSI Lane0 +

21	MIPI_DON	0	MIPI DSI Lane0 -
22	GND	Р	Ground
23	MIPI_D3P	0	MIPI DSI Lane3 +
24	MIPI_D3N	0	MIPI DSI Lane3 -
25	GND	Р	Ground
26	ID	I	LCD ID
27	RST_LCD	0	LCD RESET
28	NC		
29	VCCIO_1V8	Р	power supply 1.8V
30	VCC3V3_LCD	Р	power supply 3.3V
31	VCC3V3_LCD	Р	power supply 3.3V

2.4.8 J8 MIPI CSI (8)

VT-SBC-3568 offers a 4-lane MIPI CSI connector for connecting cameras.

Specifications: 1 x 31, 0.3mm, 1.0mm(H), RA, Bot, WDT, SMT, RoHS (LZR: FPC031003-31LG)



Pin	Name	Туре	Description
1	GND	Р	Ground
2	MIPI_CSI_D3N	А	MIPI CSI Lane3 -
3	MIPI_CSI_D3P	А	MIPI CSI Lane3 +
4	GND	Р	Ground
5	MIPI_CSI_D2N	А	MIPI CSI Lane2 -
6	MIPI_CSI_D2P	А	MIPI CSI Lane2 +
7	GND	Р	Ground
8	MIPI_CSI_D1N	А	MIPI CSI Lane1 -
9	MIPI_CSI_D1P	А	MIPI CSI Lane1 +
10	GND	Р	Ground
11	MIPI_CSI_DON	А	MIPI CSI Lane0 -
12	MIPI_CSI_D0P	А	MIPI CSI Lane0 +
13	GND	Р	Ground
14	MIPI_CSI_CLKON	А	MIPI CSI CLK-
15	MIPI_CSI_CLK0P	А	MIPI CSI CLK+
16	GND	Р	Ground

17	I2C2_SCL_M1_1V8	0	I2C_SCL
18	I2C2_SDA_M1_1V8	I/O	I2C_SDA
19	MIPI_CAM_RST	0	Camera RESET
20	MIPI_CAM_PWN	0	Camera Power Down
21	GND	Р	Ground
22	MIPI_CAM_MCLK	0	Camera Main CLK
23	GND	Р	Ground
24	NC		
25	VCC1V8_DVP	Р	Power supply 1.8V
26	VCC1V8_DVP	Р	Power supply 1.8V
27	VCC1V5_DVP	Р	Power supply 1.5V
28	VCC2V8_DVP	Р	Power supply 2.8V
29	VCC2V8_DVP	Р	Power supply 2.8V
30	NC		
31	GND	Р	Ground

2.4.9 J22 I²C (9)

VT-SBC-3568 offers an I²C interface for connecting a touch panel.

Specifications: 1 x 6, 0.5mm, 0.4A, 0.9mm(H), female, RA, WDT, SMT, RoHS (UJU: PF050-B06B-C09-A)



Pin	Name	Туре	Description
1	VCC3V3_PMU	Р	Power supply 3.3V
2	GND	Р	Ground
3	I2C1_SCL_TP	0	I2C_SCL for TP
4	I2C1_SDA_TP	I/O	I2C_SDA for TP
5	TP_INT	I	TP interrupt
6	TP_RST	0	TP RESET

2.4.10 U40 USB 2.0 Type-A (10)

VT-SBC-3568 is designed to connect peripherals via the USB 2.0 Type-A interface to expand the functions.

The pinout description of the USB 2.0 Type-A interface is in line with the pin assignment of standard USB 2.0 Type-A.



USB 2.0 Type-A

2.4.11 J3/J4/J5 USB 2.0 (11)

There are three USB 2.0 connectors, one could be used for connection of a touch panel, and the other two are in host mode by default (chipset USB2514).

Specifications: 1 x 4, 2.0mm, 2A, 6mm(H), male, RA, WDT, THR, RoHS (JST: B4B-PH-K-S (LF) (SN))



Pinout description:

Pin	Name	Туре	Description
1	GND	Р	Ground
2	HUB_HOST_DP	I/O	USB DATA+
3	HUB_HOST_DM	I/O	USB DATA-
4	HUB_HOST_5V	Р	Power supply 5V for USB

2.4.12 J6 USB 3.0 (12)

VT-SBC-3568 offers a USB 3.0 (Type-A) interface with pinout in line with the pin assignment of standard USB 3.0 Type-A.



2.4.13 J7 USB 2.0 Type-C (13)

VT-SBC-3568 offers a USB 2.0 Type-C interface.

Specifications: Max. current output: 0.5A, OTG supported.



USB 2.0 Type-C

2.4.14 J21 GPIO (14)

There is a GPIO header supporting 12 GPIOs and user can customize the use of the pins.

Specification: 2 x 10, 2.0 mm, 6.5 mm (H), Vert, -25~85°C, THR, RoHS (JST: B20B-PHDSS (LF) (SN))



Pinout description (a user case is that the pins are used for LED and buttons):

Pin	Name	Туре	Description
1	LED_1	0	LED output signal
2	BUTTON_1	I	Button input data
3	LED_2	0	LED output signal
4	BUTTON_2	I	Button input data
5	LED_3	0	LED output signal
6	BUTTON_3	I	Button input data
7	LED_4	0	LED output signal
8	BUTTON_4	I	Button input data
9	LED_5	0	LED output signal
10	BUTTON_5	I	Button input data
11	LED_6	0	LED output signal
12	BUTTON_6	I	Button input data
13	VCC3V3_EXT	Р	Power supply 3.3V
14	VCC3V3_EXT	Р	Power supply 3.3V
15	GND	Р	Ground

16	GND	Р	Ground
17	VCC_1V8	Р	Power supply 1.8V
18	VCC_1V8	Р	Power supply 1.8V
19	GND	Р	Ground
20	GND	Р	Ground

2.4.15 J20 40-pin header (15)

The 40-pin header on VT-SBC-3568 supports 2 CAN buses, 6 UART ports, and 12 GPIOs (customized for alarm in and out in the following description). Among the 6 UART ports, there are 2 RS485 ports (UART 6 & UART 7, corresponding to nodes ttyS6 & ttyS7), 1 TTL (UART 9, corresponding to ttyS9), 2 RS232 (UART 3 & UART 4, corresponding to ttyS3 and ttyS4), and 1 RS232 (UART 2) used for debugging.

Specifications: 2 x 20, 2.54mm, 8.8 mm(H), Vert, -45~105°C, SMT, RoHS



Pinout description:

Pin	Name	Туре	Description
1	CAN0_RX_M1	I	CAN0 serial port data input
3	CAN0_TX_M1	0	CANO serial port data output
5	CAN1_RX_M1	I	CAN1 serial port data input
7	CAN1_TX_M1	0	CAN1 serial port data output
9	ALARM_OUT1	0	Alarm output
11	ALARM_OUT2	0	Alarm output
13	ALARM_OUT3	0	Alarm output
15	ALARM_OUT4	0	Alarm output
17	ALARM_IN1	I	Alarm input
19	ALARM_IN2	I	Alarm input
21	ALARM_IN3	I	Alarm input
23	ALARM_IN4	I	Alarm input
25	ALARM_IN5	I	Alarm input
27	ALARM_IN6	I	Alarm input
29	ALARM_IN7	I	Alarm input

31	EXGND	Р	Ground
33	EXGND	Р	Ground
35	EXGND	Р	Ground
37	EXGND	Р	Ground
39	EXGND	Р	Ground
2	UART9_RX_M1	l.	UART9 receive data
4	UART9_TX_M1	0	UART9 transmit data
6	UART2_RX_M0_DEBUG	l.	UART2 receive data
8	UART2_TX_M0_DEBUG	0	UART2 transmit data
10	UART4_RX_M1	I	UART4 receive data
12	UART4_TX_M1	0	UART4 transmit data
14	UART3_RX_M1	I	UART3 receive data
16	UART3_TX_M1	0	UART3 transmit data
18	UART7_RX_M1	I	UART7 receive data
20	UART7_TX_M1	0	UART7 transmit data
22	RS485_DIR1_GPIO3_B5	I/O	RS485_1 control output signal
24	UART6_RX_M1	I	UART6 receive data
26	UART6_TX_M1	0	UART6 transmit data
28	RS485_DIR2_GPIO3_B6	I/O	RS485_2 control output signal
30	ALARM_IN8	I	Alarm input
32	DC_IN	Р	Power input
34	DC_IN	Р	Power input
36	DC_IN	Р	Power input
38	DC_IN	Р	Power input
40	DC IN	Р	Power input

2.4.16 J17 M.2 Key B slot (16)

VT-SBC-RK3568 offers an M.2 Key B slot, supporting PCIe to connect a 3G/4G/5G module and supporting SATA.



Pinout description:

Pin	Name	Туре	Description	Comment
1, 20, 22, 23, 24, 28, 29, 31, 35, 37, 38, 40, 42, 44, 46, 48, 56, 58, 59, 60, 61, 62, 63, 64, 65, 66, 68	NC	NC		
2, 4, 70, 72, 74	PCIE_3V3	Р		
3, 5, 11, 27, 33, 39, 45, 51, 57, 71, 73, 76, 77	GND	Ρ	Ground	
36	5G_USIM_PWR	Р	USIM power output	Fither 1.8 V
34	5G_USIM_DATA	I/O	USIM data signal	or 3.0 V is
32	5G_USIM_CLK	I	USIM clock signal	supported by
30	5G_USIM_RESET	I	USIM reset signal	the module
66	SIM_DETECT1	0	USIM Plug Detect signal	automatically.
7	USBDP_DN4	Ю	USB data+	
9	USBDM_DN4	Ю	USB data-	
41	PCIE_RXN_A	0	PCIE 2.0 transmit data-	
43	PCIE_RXP_A	0	PCIE 2.0 transmit data+	
47	PCIE_TXN_A	I	PCIE 2.0 receive data-	
49	PCIE_TXP_A	I	PCIE 2.0 receive data+	
53	PCIE_CLKN_A	I	PCIE 2.0 CLK-	
55	PCIE_CLKP_A	I	PCIE 2.0 CLK+	
6	5G_PWR_OFF#1_1V8	I	5G power on and off control	1.9.1 рошат
8	5G_DISABLE#1_1V8	0	5G Flight mode control data	domain
67	WWAN_RESET#1	I	5G RESET	

2.4.17 J16 Audio jack (17)

VT-SBC-RK3568 offers a 3.5mmaudio jack.



2.4.18 J15 Speaker connector (18)

VT-SBC-RK3568 offers a left/right speaker connector that is designed to connect a $5W/8\Omega$ speaker.

specifications: 1 x 4, 2.0mm, 2A, 6mm (H), male, Vert, WDT, THR, RoHS (JST: B4B-PH-K-S (LF) (SN))



Pinout description:

Pin	Name	Туре	Description
1	OUTL+A	А	5W speaker +
2	OUTL-A	А	5W speaker -
3	OUTR-A	А	5W speaker -
4	OUTR+A	А	5W speaker +

2.4.19 Buttons (19-21)

VT-SBC-3568 offers three buttons, including a volume button, an on/off button, and a reset button.



2.4.20 J18 Micro SIM (22)

VT-SBC-3568 offers a Micro SIM card slot.

Specifications: Micro SIM, push-push, -25°C~90°C, No WP, SMT, RoHS

CHAPTER 3



3.1 Prerequisites

Whenever a new image is available, Vantron will provide a release package consisting of all the tools/files necessary so that you can flash the image in the Windows and Ubuntu environments.

Please prepare the following:

- A host with terminal tools installed (running Windows 7 or later or Ubuntu 16.4 or later)
- USB cable
- Connect the host and VT-SBC-3568 with the USB cable

3.2 Firmware Upgrade in Loader Mode (Windows system)

3.2.1 Driver installation

In windows environment, follow the steps below to finish driver installation.

- 1. Unzip the release package sent from Vantron, and open the folder;
- 2. Navigate to the following directory: SW/ downloadetools/ windows/ DriverAssitant_v4.91/ DriverInstall;



- 3. Right click the mouse and run the program as administrator;
- 4. Click Install the Driver and wait;



5. A pop-up will appear in a second suggesting the driver is installed;

S RK Driver Assitant v5.1.1				
	DriverInstall	×		
Instal	Install driver ok.		ll Driver	
	ОК			

3.2.2 ADB setup

- 1. Open the release package and navigate to the following directory: SW/ downloadetools/ windows/ adbtoolswindos/ platform-tools to locate **adb**.exe;
- 2. Press "Win + R" and input "sysdm.cpl" in the dialogue box to open the settings interface;

💷 Run	×
	Type the name of a program, folder, document, or Internet resource, and Windows will open it for you.
<u>O</u> pen:	sysdm.cpl 🗸
	OK Cancel <u>B</u> rowse

 Click in sequence Advanced > Environment Variables > Path > Edit, and click New in the pop-up;

- mare - course (c) - mak / maxim / saladamin				(A) Search based (1940) (2)
stem Properties X	User variables for johu		Edit environment variable	
Annord Name Hardware Annord System Protection Remote You mult be logged on as an Ammittrator to make most of these changes. Performance Vauel effects, processor scheduling, memory usage, and vitual memory Settings User Profiles Constructions with a for an an amount	Variable Valuer OnoDrive CVUJenskýshu/OnoDri path CXProgram File/Stan TEMP %USERPROFILE%VApp TMP %USERPROFILE%VApp	ve disipi pOtatilocaliTemp DatailocaliTemp	SkystemRootSvipstem32 SkystemRootS SkystemRootSvipstem32/Whem SkystemRoOTSvipstem32/WindowsPowerShe	Hvr1.01. Browse Delete
Settings.	System variables	New Edit Delete		Move Up Move Down
Statup and Recovery System statup, system falure, and debugging information Settings	Variable Value OS Windows,NT Path C/Windows/system3, PATHEXT .COM;EXE;BAT;CMD	2;C:\Windows;C:\Windows\System32\Wbem; },VBS;NBE_JS;JSE.WSF;,WSH:MSC		Edit test
Environment Variables	PROCESSOR_ARCHITECTURE AMD64 PROCESSOR_IDENTIFIER Intel64 Family 6 Mode PROCESSOR_LEVEL 6 PROCESSOR_REVISION 9e8a	el 158 Stepping 10, Genuineintel		
		New Edit Delete		OK Cancel
		01		

4. Copy the path of **adb** .exe as mentioned in step 1, and click **OK** to confirm;

dit environment variable	>
%SystemRoot%\system32	New
%SystemRoot%	
%SystemRoot%\System32\Wbem	Edit
%SYSTEMROOT%\System32\WindowsPowerShell\v1.0\	
C:\work\windows\adbtoolswindos\platform-tools	Browse
	Delete
	Move Up
	Move Down
	Edit text
ОК	Cancel

- 5. Press "Win + R" and input "cmd" in the dialogue box;
- 6. Input "adb --version" in the command prompt window to check if the ADB tool is installed.



3.2.3 Image download

- 1. Connect VT-SBC-3568 and the host with the USB cable;
- 2. Input "adb devices -I" in the command prompt window to check if VT-SBC-3568 is connected to thehost;



- 3. Navigate to the local directory as follows: SW/ downloadetools/ windows/ RKDevTool_Release_v2.84, and double click RKDevTool.exe 🔀 RKDevTool ;
- 4. Normally, a message suggesting the existence of an ADB device will show up at the bottom of the tool window in about 10 seconds;
- 5. Input "adb shell" in the prompt window to enter ADB shell so that you can execute shell commands on the device;

C:\Users\jxh <mark>u≻ad</mark>	b shell		
sh-5.0#			
sh-5.0# ls			
UDC	bMaxPacketSize0	functions	strings
bDeviceClass	bcdDevice	idProduct	
bDeviceProtocol	bcdUSB	idVendor	
bDeviceSubClass	configs	os_desc	
sh-5.0# 🛓			

6. Input "reboot loader" in ADB shell to enter Loader mode;



7. Go back to RK Device Tool window, you'll see a message suggesting the existence of a loader device at the bottom;

#		Address	Name	Path	
1	Г	0x00000000	Loader		
2		0x00000000	Parameter		
3		0x00000000	Vboot		
4		0x00000000	trust		
5		0x00000000	Misc		
6		0x00000000	Resource		
7	느	0x00000000	Kernel		
8		0x00000000	Boot		
9	느	0x00000000	Recovery		
10	느	0x00000000	System		
11		0x00000000	Backup		
<					>
0.00	tor		Run	Switch Dev Partition	Clear

- 8. Click **Upgrade Firmware** tab (the second tab at the top of the window) and then **Firmware** button;
- Select update.img from the directory: SW\ Image and click Open, and the firmware details will be automatically populated in the firmware information box;
- 10. Click **Upgrade** button (next to **Firmware** button), and the device will start to download the image and upgrade;

RKDevTool v2.84			-	×
Download Image Upgrade Firmware Advanced Function Firmware Upgrade Switch EraseFlash Fw Ver: 1.0.00 Loader Ver: 1.01 Firmware: C:\work\update.img	Chip Info: RK3568	Test Device Start Test Device Success Check Chip Start Check Chip Success Get FlashInfo Start Get FlashInfo Success Frepare IDB Start Download IDB Success Download Firmware Start Download Firmware (6%)		
Found One LOADER Dev	vice			

11. When the upgrade finishes, the device will reboot automatically.

3.3 Firmware Upgrade in Loader Mode (Ubuntu system)

3.3.1 ADB setup

1. Input the following command line in Ubuntu terminal to install ADB tool;

Ş sudo apt-get install adb -y
jxhu@vantron:~\$ sudo apt-get install adb
[sudo] password for jxhu:
Reading package lists Done
Building dependency tree
Reading state information Done
adb is already the newest version (1:8.1.0+r23-5ubuntu2).
The following packages were automatically installed and are no long
binutils-aarch64-linux-gnu binutils-arm-linux-gnueabihf cpp-9-aar
gcc-9-arm-linux-gnueabihf-base gcc-9-cross-base libasan5-arm64-cr
libgcc-9-dev-arm64-cross libgcc-9-dev-armhf-cross libgcc-s1-arm64
libstdc++6-armhf-cross libtsan0-arm64-cross libubsan1-arm64-cross
Use 'sudo apt autoremove' to remove them.
0 upgraded, 0 newly installed, 0 to remove and 49 not upgraded. ixhu@vantron:~S

- 2. Connect VT-SBC-3568 and the host with the USB cable;
- 3. Check if VT-SBC-3568 is connected to the host;



4. Input "adb shell" to enter ADB shell so that you can execute shell commands on the device;

ixhu@vantron.~\$	adh shell		
	hMaxDackatcizaa	functions	stripes
	bedDowies	idDeeduct	strugs
	beduep	tarroduct	
DDeviceProtocol	DCOUSB	lavendor	
bDeviceSubClass	configs	os_desc	
sh-5.0# 📋			

5. Input "reboot loader" in ADB shell, and the device will reboot automatically and enter the loader mode.



3.3.2 Image Download

- Locate the local directory of the upgrade tool in the release package, i.e., SW\ downloadetools\linux\Linux_Upgrade_Tool;
- 2. Use the upgrade tool: copy the release package to Ubuntu system from the local directory, or save the release package in a USB drive and open it with command lines in Ubuntu, or open the terminal from the directory of the upgrade tool in Ubuntu, choose whichever convenient for you;

< → 700RPAGBH.	M,20220322	SW download	letools linux	Linux_Upgrade_Tool	-
🕚 Recent					
★ Starred	log	config.ini	revision.txt	upgrade_	
습 Home				τοοι	

3. Input the following command line in the terminal to download the upgrade image;

\$./upgrade_tool uf xxx/SW/update.img

"xxx" is the full name of the release package.

4. The system will start upgrading right the download finishes, and the system will reboot automatically when the upgrade finishes.

3.4 Development Guide

3.4.1 Static IP setup

- Input the following command in the terminal for network setup
 \$ vi /etc/network/interfaces
- 2. Example:

\$ auto eth0
\$ iface eth0 inet static
\$ address 192.168.1.111
\$ netmask 255.255.255.0
\$ gateway 192.168.1.1

\$ reboot

3.4.2 Qt setup for cross compilation

1. Install Qt installer (Ubuntu 20.04 is recommended);

```
$ dpkg -i ubuntu-host-qt-5.12.11.deb
```



2. Run the installer and click **Tool -> Options ->Kits** in sequence, then add the compilers that have been installed: aarch64-linux-gnu-gcc, aarch64-linux-gnu-g++, aarch64-linuxgnu/bin/aarch64-linux-gnu-gdb.

Filter	Kits Compilers		
Kits	构建套件(Kit) Qt Versions 编译器 Debuggers Qbs CMake		Add
🖵 环境	Name	Туре	· 动态 ·
■ 文本编辑器	 Auto-detected C 		Linux ICC
K. FakeVim	GCC (C, x86 64bit in /usr/bin) GCC (C, x86 32bit in /usr/bin)	GCC	MinGW
2 帮助	GCC 5 (C, x86 64bit in /usr/bin) GCC 5 (C, x86 32bit in /usr/bin)	CCC C	GCC
() C++	GCC (C, x86 64bit in /usr/bin)	GCC	Custom
1 Ot Ouick	GCC 5 (C, x86 64bit in /usr/bin)	GCC	QCC
> 构建和运行	GCC 5 (C, x86 32bit in /usr/bin) Clang (C, x86 64bit in /usr/bin)	GCC Clang	
 ▲ (四)/	Clang (C, x86 32bit in /usr/bin) Clang 3.8 (C, x86 6dbit in /usr/bin)	Clang	
 Detailer 	Clang 3.8 (C, x86 32bit in /usr/bin)	Clang	
设计师	GCC (C, arm 64bit in /usr/bin) GCC 4.9 (C, arm 64bit in /usr/bin)	GCC	
▶ 分析器	GCC 5 (C, arm 64bit in /usr/bin)	GCC	
助版本控制	Clang (C++, x86 64bit in /usr/bin/Qt5.12.1/160is/QtCreator/liber Clang (C++, x86 32bit in /usr/bin/Qt5.12.1/Tools/QtCreator/liber	ec/qtcreator/clang/bin) Clang ec/qtcreator/clang/bin) Clang	
7 设备	 C++ GCC (C++ x86 64bit in /usr/bin) 	666	
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in reading	GCC (C++, x86 64bit in /usr/bin)	GCC	
	GCC (C++, X80 32DIC In /USI/DIN)	CCC CCC	
	GCC 5 (C++, x86 32bit in /usr/bin)	GCC	
	Clang (C++, x86 64bit in /usr/bin)	Clang	
	Clang (C++, x86 32bit in /usr/bin)	Clang	
	Clang 3.8 (C++, x86 64bit in /usr/bin)	Clang	
	Clang 3.8 (C++, x86 32bit in /usr/bin)	Clang	
	GCC (C++, arm 64bit in /usr/bin)	GCC	
	GCC 4.9 (C++, arm 64bit in /usr/bin)	GCC	
	* Manual	occ	
	* C		*
		Apply 1	Cancel 🖌 🖌 OK

3. Configure GCC (path: opt/gcc-tool/gcc-linaro-7.4.1-2019.02-x86_64_aarch64-linuxgnu/bin/aarch64-linux-gnu-gcc);

				这项——QE	reacor			•
Filter	Kits							
🔛 Kits	Kits Qt Versions 编	译器 Debuggers	CMake					
□ 环境	Name				Туре			* 添加 *
文本编辑器	GCC (C, x86 64b GCC 7 (C, x86 64	it in /usr/lib/ccache) bit in /usr/lib/ccach	•)		GCC			支持
K. FakeVim	GCC 9 (C, x86 64	bit in /usr/lib/ccach	:)		GCC			
@ 帮助	GCC (C++, x86 6	4bit in /usr/bin)			GCC			劉隊
{} C++	GCC (C++, x86 6	4bit in /usr/lib/ccach	e)		GCC			Remove All
1 Ot Ouick	GCC 9 (C++, x86	64bit in /usr/lib/cca	the)		GCC			Re-detect
> 构建和运行	GCC (C++, x86 6- GCC 7 (C++, x86	4bit in /usr/lib/ccach 64bit in /usr/lib/cca	e) :he)		GCC			Auto-detection Settings
C Obs	GCC 9 (C++, x86	64bit in /usr/lib/cca	the)		GCC			
·	✓ C yt.hmi19.3568.c	<i>a</i>			666			
2 3034 (F	VT-DISP-ECO-GO	C			GCC			
P Duthes	vt-hmi19-3568-c	rt		ABI	rm-linux-			
E 万机器								
■ NX412#1	名称:	vt-hmi19-3568-cr						
10 设备	编译器路径(C):	/opt/gcc-tool/gcc	linaro-7.4.1-2019.02	-x86 64 aarch64-linux-gnu	/bin/aarch64-linux-gnu-gcd		浏览	
11 11 11 11 11 11 11 11 11 11 11 11 11	Platform codegen flags							
e Language Client	Platform linker flags:							
1 Testing	ABI:	arm-linux-generie	elf-64bit		• arm linux	~]- generic elf	64bit -	
							S A∣	opiy <u>Scancel</u> <u>OK</u>

4. Configure G++ (path: /opt/gcc-tool/gcc-linaro-7.4.1-2019.02-x86_64_aarch64linuxgnu/bin/aarch64-linux-gnu-g++);

			选项 — Qt Creator		
ilter	Kits				
Kits	Kits Qt Versions 编词	목왕용 Debuggers CMake			
□ 环境	Name		Туре	-	添加 •
■ 文本编辑器	GCC (C, x86 64bit GCC 7 (C, x86 64bit	: in /usr/lib/ccache) bit in /usr/lib/ccache)	GCC		+ 04
FakeVim	GCC 9 (C, x86 64	bit in /usr/lib/ccache)	GCC		兄隆
2 表示目的	GCC (C++, x86 64	bit in /usr/bin)	GCC		删除
Cu Cu	GCC 7 (C++, x86 6 GCC (C++, x86 64	64bit in /usr/bin) bit in /usr/lib/ccache)	GCC GCC		Remove All
./ C++	GCC 7 (C++, x86 0	54bit in /usr/lib/ccache)	GCC		Re-detect
W QEQUICK	GCC (C++, x86 64	bit in /usr/lib/ccache)	GCC		Auto-detection Settings
▶ 构建和运行	GCC 7 (C++, x86 6 GCC 9 (C++, x86 6	64bit in /usr/lib/ccache) 64bit in /usr/lib/ccache)	GCC		
∰ Qbs	* Manual				
₿ 调试器	vt-hmi19-3568-cr	t	GCC		
/ 设计师	▼ C++		GCC		
Python	vt-hmi19-3568-cr	t	GCC		
■ 分析器					
0 版本控制	da 100.	the herita 2560 set			
-0 设备	百初:	Venning-3368-cre			
自 代码转账	编译器路径(<u>C</u>):	opt/gcc-tool/gcc-linaro-7.4.1-2019.02-x	x86_64_aarch64-linux-gnu/bin/aarch64-linux-gnu-g++	浏览	
	Platform codegen flags:				
Testing	Platform linker flags:				
IA resung	ABI:	arm-linux-generic-elf-64bit	▼ arm × linux × generic × elf	* 64bit *	

5. Configure GDB (path: /opt/gcc-tool/gcc-linaro-7.4.1-2019.02-x86_64_aarch64-linuxgnu/bin/aarch64-linux-gnu-gdb);

					选项 — Qt Creator				8
Filter	Kits Qt Versions	编译器 Deb	uggers Cl	CMake					
 □ 环境 □ 末境 □ 末嶋福器 ※ FakeVim ● 常助 () C++ イ Qt Quick ▶ 构連和运行 Ⅲ Qbs ● 資減器 ▶ () 计师 ● Python 反分析器 () 版本控制 	Name Atto-detected System CDB at System CDB	Lo /usr/bin/gdb /u /bin/gdb /b DB /h DB /h crt /o	ocation srs/bin/gdb in/gdb iome/jxhu/d jpj/gcctool/	doc/VT-Di /gcc-linar	0ISP-ECO/qtFile/firefly-fk3308b-aarch64/host/bin/aarch644 ro-7.4.1-2019.02:x86_64_aarch64-linux-gnu/bin/aarch64-lin	iinux-gdb itux-gnu-gdb	Type CDB CDB CDB B CDB		Add Clone Remove
대 设备 ① 代码粘贴 d [®] Language Client 【 Testing	Name: Path: Type: ABIs: Version: Working directory:	vt-hmi19-3568 /opt/gcc-tool/g GDB arm-linux-gene 2019.2.0	-crt gcc-linaro-7.4 eric-elf-64bit	.4.1-2019.	3.02×86_64_aarch64-linux-gnu/bin/aarch64-linux-gnu-gdb			》览 》览	
							ØA	ply Scance	l 📀 <u>о</u> к

6. Click **Qt Versions** next to **Compiler** to configure qmake;

	选项 — Qt Creator		
ilter	Kits Kits Qt Versions 编译器 Debuggers CMake		
Alc 环境 文本编辑器 尾 FakeVim 前助 () C++ 文以はた 内数和近行 四 Qbs 通ば器 公 投行师 会 Python 匠 分析器 ① 设备 ① 设备 【* 代码粘胎 CP Language Client L Testing	Name * (marke Location * Auto-detected (marke Location QE 5.12.11 CC 66bit: (home/jshu/QE5.12.11/s1c_64/bin/qmake * Menuel (QE 5.9.4 Elice\$17.811)/cpt/QE5PAEL/Str/QmAX QE 5.9.4 Elice\$17.8110/cpt/QE5PAEL/Str/QmAX QE 5.9.4 Elice\$17.8110/cpt/QE5PAEL/Str/QmAX	添加 對除 Link with Qt Clean Up	
	版本名称 Qt%(Qt.Version) (qt5.12.11)		
	qmake 路径: /opt/qt5.12.11/bin/qmake 浏览		
	qmake path: 桌面的Qt 版本5.12.11 详细	*	
	Register documentation: All +		
		ancel OOK	

7. Add the build kit, including name, device type, complier, and Qt version.

		透现 — Qt Creator	
Filter	Kits Kits Qt Versions 编译器	Debuggers CMake	
□ 环境	8年		
■ 文本编辑器	* Auto-detected		
FakeVim	* Manual F		
8 帮助	vt hmi10 rk99 ls	Make	Default
{} C++	Vt-nmi19-3568-crt	Settin	gs Filter
A Qt Quick		Default Se	ttings Filter
▶ 构建和运行			
I Qbs	名称:	vt-hmi19-3568-crt	₽.
● 调试器	File system name:		
/ 设计师	Device type:	医 泉	1
Python	Device:	Local PC (桌面 类型的默认设备) *	Manage
፪ 分析器	Sysroot:		浏览
1 版本控制	Complex.	C: vt-hmi19-3568-crt -	Manage
□ 设备	compiler.	C++: vt-hmi19-3568-crt	
直 代码粘贴	Environment:	No changes to apply.	Change
Language Client	Debugger:	vt-hmi19-3568-crt -	Manage
Testing	Qt version:	Qt 5.12.11 (qt 5.12.11)	Manage
	Qt mkspec:		
	Additional Qbs Profile Setting		Change
	CMake Tool:	System CMake at /usr/bin/cmake	Manage
	CMake generator:	<none> - Unix Makefiles, Platform: <none>, Toolset: <none></none></none></none>	Change
	CMake Configuration:	CMAKE_CXX_COMPILER:STRING=%[Compiler:Executable:Cxx]; CMAKE_C_COMPILER:STRING=%(Compiler:Executable:C); CMAKE_PREFIX_PATH:STRING=%(Qt:	Change
		S Apply S Canc	el Ø <u>o</u> k

3.5 Use MySQL

- 1. Log in MySQL; mysql -uroot
- Use MySQL database; use mysql;
- 3. Set passwords for rootuser;
 SET PASSWORD FOR 'root'@'localhost' = PASSWORD('123456');
- Allow remote connection;
 update user set host='%' where user='root' and host='localhost';
 FLUSH PRIVILEGES;

3.6 Interface Configuration

3.6.1 Serial port

VT-SBC-3568 offers 6 UART ports, including 2 RS485 ports (UART 6 & UART 7, corresponding to nodes ttyS6 & ttyS7), 1 TTL (UART 9, corresponding to ttyS9), 2 RS232 (UART 3 & UART 4, corresponding to ttyS3 and ttyS4), and 1 RS232 (UART 2) used for debugging.

1. Input the node of the serial port to enable serial communication (e.g., RS485_1);

rs485_active /dev/ttyS6

2. Send data ("TEST") to the serial port and receive the data.

echo TEST > /dev/ ttyS6

cat /dev/ ttyS6 &

b the serial port cannot receive and transmit data at the same time.

3.6.2 CAN

VT-SBC-3568 offers two CAN buses (CAN0 & CAN1).

Configure the CAN buses and run the commands:

UP RUNNING NOARP MTU:16 Metric:1 RX packets:12 errors:0 dropped:0 overruns:0 frame:0 TX packets:5 errors:0 dropped:0 overruns:0 carrier:0 collisions:0 txqueuelen:10 RX bytes:44 (44.0 B) TX bytes:20 (20.0 B) Interrupt:32 can1 Link encap:UNSPEC HWaddr 00-00-00-00-00-00-00-00-00-00-00-00-00 UP RUNNING NOARP MTU:16 Metric:1 RX packets:12 errors:0 dropped:0 overruns:0 frame:0 TX packets:7 errors:0 dropped:0 overruns:0 carrier:0 collisions:0 txqueuelen:10 RX bytes:44 (44.0 B) TX bytes:24 (24.0 B) Interrupt:33

3.6.3 GPIO

 Write a GPIO pin number to "/sys/class/gpio/export" to export the pin, for instance pin value = 20:

```
~# echo 20 > /sys/class/gpio/export
```

2. Set the pin direction as input or output (in for input and out for output);

```
~# echo out > /sys/class/gpio/gpio20/direction
```

3. If you configured an output pin in the prior step, now you can set its value to 0 or 1 (corresponding to low or high) as follows:

```
~# echo 0 > /sys/class/gpio/gpio20/value [set it low], or
```

```
~# echo 1 > /sys/class/gpio/gpio20/value [set it high]
```

4. Read the GPIO value;

```
~# cat /sys/class/gpio/gpio20/value
```

5. When you finish using the pin, just unexport it. To do this, write the pin number to the unexport file:

```
~# echo 20 > /sys/class/gpio/unexport
```

3.6.4 Ethernet

Set up and verify the IP address of the ethernet port:

ifconfig eth0 192.168.9.10
ping 192.168.9.10
PING 192.168.9.10 (192.168.9.10): 56 data bytes
64 bytes from 192.168.9.10: seq=0 ttl=64 time=1.296 ms
64 bytes from 192.168.9.10: seq=1 ttl=64 time=1.358 ms

CHAPTER 4

DISPOSAL AND PRODUCT WARRANTY

4.1 Disposal

When the device comes to end of life, you are suggested to properly dispose of the device for the sake of the environment and safety.

Before you dispose of the device, please back up your data and erase it from the device.

It is recommended that the device is disassembled prior to disposal in conformity with local regulations. Please ensure that the abandoned batteries are disposed of according to local regulations on waste disposal. Do not throw batteries into fire or put in common waste canister as they are explosive. Products or product packages labeled with the sign of "explosive" should not be disposed of like household waste but delivered to specialized electrical & electronic waste recycling/disposal center.

Proper disposal of this sort of waste helps avoid harm and adverse effect upon surroundings and people's health. Please contact local organizations or recycling/disposal center for more recycling/disposal methods of related products.

4.2 Warranty

Product warranty

VANTRON warrants to its CUSTOMER that the Product manufactured by VANTRON, or its subcontractors will conform strictly to the mutually agreed specifications and be free from defects in workmanship and materials (except that which is furnished by the CUSTOMER) upon shipment from VANTRON. VANTRON's obligation under this warranty is limited to replacing or repairing at its option of the Product which shall, within <u>24 months</u> after shipment, effective from invoice date, be returned to VANTRON's factory with transportation fee paid by the CUSTOMER and which shall, after examination, be disclosed to VANTRON's reasonable satisfaction to be thus defective. VANTRON shall bear the transportation fee for the shipment of the Product to the CUSTOMER.

Out-of-Warranty Repair

VANTRON will furnish the repair services for the Product which are out-of-warranty at VANTRON's then-prevailing rates for such services. At customer's request, VANTRON will provide components to the CUSTOMER for non-warranty repair. VANTRON will provide this service as long as the components are available in the market; and the CUSTOMER is requested to place a purchase order up front. Parts repaired will have an extended warranty of 3 months.

Returned Products

Any Product found to be defective and covered under warranty pursuant to Clause above, shall be returned to VANTRON only upon the CUSTOMER's receipt of and with reference to a VANTRON supplied Returned Materials Authorization (RMA) number. VANTRON shall supply a RMA, when required within three (3) working days of request by the CUSTOMER. VANTRON shall submit a new invoice to the CUSTOMER upon shipping of the returned products to the CUSTOMER. Prior to the return of any products by the CUSTOMER due to rejection or warranty defect, the CUSTOMER shall afford VANTRON the opportunity to inspect such products at the CUSTOMER's location and no Product so inspected shall be returned to VANTRON unless the cause for the rejection or defect is determined to be the responsibility of VANTRON. VANTRON shall in turn provide the CUSTOMER turnaround shipment on defective Product within **fourteen (14) working days** upon its receipt at VANTRON. If such turnaround cannot be provided by VANTRON due to causes beyond the control of VANTRON. VANTRON shall document such instances and notify the CUSTOMER immediately.