# VT-MITX-APL Single Board Computer



# **User Manual**

Version: 1.3

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# **Revision History**

| No. | Version | Description               | Date          |
|-----|---------|---------------------------|---------------|
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| 2   | V1.1    | Updated specifications    | Apr. 23, 2022 |
| 3   | V1.2    | Updated interface figures | Nov. 20, 2022 |
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# Foreword

Thank you for purchasing VT-MITX-APL single board computer ("the Board" or "the Product"). This manual intends to provide guidance and assistance necessary on setting up, operating or maintaining the Product. Please read this manual and make sure you understand the functionality of the Product before putting it into use.

### **Intended Users**

This manual is intended for:

- Embedded software developer
- Custom development software engineer
- Other technically qualified personnel

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It is our practice to change part numbers when published ratings or features are changed, or when significant construction changes are made. However, some specifications of the Product may be changed without notice.

### **Technical Support and Assistance**

Should you have any question about the Product that is not covered in this manual, contact your sales representative for solution. Please include the following information in your question:

- Product name and PO number;
- Complete description of the problem;
- Error message you received, if any.

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### Symbology

This manual uses the following signs to prompt users to pay special attention to relevant information.

| Â | Caution for latent damage to system or harm to personnel |  |
|---|--|--|
| ì | Attention to important information or regulations        |  |

#### **General Safety Instructions**

The Product is supposed be installed by knowledgeable, skilled persons familiar with local and/or international electrical codes and regulations. For your safety and prevention of damage to the Product, please read and observe carefully the following safety instructions prior to installation and operation. Keep this manual well for future reference.

- Do not disassemble or otherwise modify the Product. Such action may cause heat generation, ignition, electronic shock, or other damages including human injury, and may void your warranty.
- Keep the Product away from heat source, such as heater, heat dissipater, or engine casing.
- Do not insert foreign materials into any opening of the Product as it may cause the Product to malfunction or burn out.
- To ensure proper functioning and prevent overheating of the Product, do not cover or block the ventilation holes of the Product.
- Follow the installation instructions with the installation tools provided or recommended.
- The use or placement of the operation tools shall comply with the code of practice of such tools to avoid short circuit of the Product.
- Cut off the power before inspection of the Product to avoid human injury or product damage.

#### **Precautions for Power Cables and Accessories**

- Use proper power source only. Make sure the supply voltage falls within the specified range.
- Place the cables properly at places without extrusion hazards.
- There is a coin cell battery for powering the RTC. Therefore, please avoid short circuit of the battery during transportation or operation at high temperatures.
- Cleaning instructions:
  - Power off before cleaning the Product
  - Do not use spray detergent
  - Clean with a damp cloth
  - Do not try to clean exposed electronic components unless with a dust collector

Power off and contact Vantron technical support engineer in case of the following faults:

- The Product is damaged
- The temperature is excessively high
- Fault is still not solved after troubleshooting according to this manual

Do not use in combustible and explosive environment:

- Keep away from combustible and explosive environment
- Keep away from all energized circuits
- Unauthorized removal of the enclosure from the device is not allowed
- Do not change components unless the power cable is unplugged
- In some cases, the device may still have residual voltage even if the power cable is unplugged. Therefore, it is a must to remove and fully discharge the device before replacement of the components

# **CHAPTER 1 INTRODUCTION**

# **1.1 Product Overview**

VT-MITX-APL single board computer adheres to the international industry size standards with a 170mm x 170mm form factor. It is powered by Intel<sup>®</sup> Celeron<sup>®</sup> APL-N3350 quad core processor, and customers have the choice between Windows 10 and Linux operating systems. The single board computer supports multi-channel display outputs and high-definition video encoding and decoding technology to provide outstanding visual experience. Better yet, it provides rich on-board interfaces and customer expansion options to meet different application scenarios including smart retail, self-service terminals, industrial automation, intelligent medical health, and digital media.

Featuring high flexibility and high performance, the motherboard could work under extreme environments with extended temperatures ranging from  $-20^{\circ}$ C to  $+60^{\circ}$ C, making it a reliable industrial IoT solution.

| Terminology/Acronym | Description                   |  |
|---------------------|-------------------------------|--|
| NC                  | No connection                 |  |
| VCC                 | Voltage common collector      |  |
| GND                 | Ground                        |  |
| P (+)               | Positive of difference signal |  |
| N (-)               | Negative of difference signal |  |
| #                   | Active low signal             |  |
| I                   | Input                         |  |
| 0                   | Output                        |  |
| I/O                 | Input/output                  |  |
| Р                   | Power or ground               |  |
| A                   | Analog                        |  |
| OD                  | Open drain                    |  |
| PCle                | PCI express signal            |  |
| MDI                 | Media dependent interface     |  |
| BKL                 | Backlight control             |  |

## 1.2 Terminology/Acronym

# 1.3 Block Diagram



# 1.4 Specifications

| VT-MITX-APL    |                  |  |  |  |  |
|----------------|------------------|--|--|--|--|
| Guitan         | СРИ              | Intel <sup>®</sup> Celeron <sup>®</sup> , APL-N3350, Quad-core processor, 2.4GHz (Max.)<br>(Optional: N4200)                                 |  |  |  |
| System         | Memory           | DDR3L SO-DIMM socket, 18   | 866 MHz, up to 8GB                           |  |  |
|                | Storage          | 1 x SATA 3.0   |  |  |  |
| Communication  | Ethernet         | 2 x RJ45, 10/100/1000Mbp   | S  |  |  |
| Media          | Display          | 1 x HDMI 1.4b, 3840 x 2160 @30Hz<br>1 x Dual-channel LVDS, 1920 x 1200 @30Hz<br>1 x VGA, 1920 x 1200 @60Hz                                   |  |  |  |
|                | Audio            | 1 x 3.5mm Audio jack<br>1 x 3.5mm Mic jack   | 1 x Audio connector<br>2 x Speaker connector |  |  |
|                | Serial           | 2 x RS232 connector  | 2 x RS232/RS422/RS485 connector              |  |  |
|                | USB              | 2 x USB 2.0 Type-A<br>2 x USB 3.0 Type-A   | 4 x Built-in USB 2.0                         |  |  |
| I/Os           | GPIO             | 8 x GPIO   |  |  |  |
|                | SMBus            | 1 x SMBus  |  |  |  |
|                | RTC              | Supported  |  |  |  |
|                | Others           | 1 x PS/2 connector   | 2 x Standard fan connector                   |  |  |
| Expansion      | Slot             | 1 x Full Mini-PCIe for 4G with a SIM slot<br>1 x M.2 B-key (2242, PCIe x4/SATA for SSD expansion, or 305<br>PCIe x1/USB3.1 for 5G expansion) |  |  |  |
| Security       | TPM              | 1 x TPM  |  |  |  |
| <u> </u>       | Button           | 1 x Standard power/reset button  |  |  |  |
| System Control | Indicator        | 1 x Status LED   |  |  |  |
|                | Innut            | 12V DC   |  |  |  |
| Power          | input            | 1 x Power jack   | 1 x Power connector (2 x 2 x 4.2mm)          |  |  |
|                | Consumption      | 10W+   |  |  |  |
| Softwara       | Operating system | Windows 10, Linux  |  |  |  |
| Soltware       | OTA tool         | BlueSphere OTA   |  |  |  |
| Mechanical     | Dimensions       | MITX standard board, 170r  | nm x 170mm                                   |  |  |
| MECHAIICAL     | Heat dissipation | 2 x Fan connector  |  |  |  |
| Environmont    | Temperature      | Operating: -20°C~+60°C   |  |  |  |
| Condition      | Humidity         | RH 10%-85% (Non-condens  | RH 10%-85% (Non-condensing)                  |  |  |
|                | Certification    | RoHS   |  |  |  |

# 1.5 Operating System

VT-MITX-APL supports Windows 10 and Linux operating systems.

# **1.6 Mechanical Dimensions**

• 170mm x 170mm



# 1.7 Power Supply and Consumption

VT-MITX-APL works with +12V DC power input supplied by the power connector or power jack.

The Board consumes 10W+ (without speakers) of power or 40W+ (with speakers) of power. It should be pointed out that power consumption is largely determined by the RAM, storage capacity, and other configurations of the board.

# **1.8 Environmental Specifications**

VT-MITX-APL works at a temperature ranging from -20  $^{\circ}{\rm C}$  to +60  $^{\circ}{\rm C}$  and at relative humidity of 5%-95% for non-condensing purpose.

# **CHAPTER 2 CONNECTORS AND PIN ASSIGNMENT**

# 2.1 Product Layout



The board I/Os will be described in detail in 2.4 Connectors and Jumpers following the sequencing numbers provided here.

## 2.2 Memory

VT-MITX-APL is equipped with a DDR3L SO-DIMM socket that supports up to 8GB RAM.

# 2.3 Identification of Pin 1

Unless otherwise stated, pin 1 of a connector is seated on a square pad that is different from the round pads used for other pins. Sometimes, pin 1 is next to a trigonal mark on the board. When there are two rows of pins on a connector, the row with pin 1 is composed of odd numbers and the other is composed of even numbers.





Usually, there will be numbers or marks next to the pins of a connector on the board to indicate the pinouts.



## 2.4 Connectors and Jumpers

This section is going to brief the connectors/jumpers on the Board with corresponsive pinout description.

#### 2.4.1 Power jack (1)

The power jack supports 12V DC power supply, and the recommended current is 1.5A.

#### 2.4.2 J5 Power connector (2)

Specification: 2 x 2 x 4.2mm, 12.8mm (H), Male, Vertical, White, WDT, THR, RoHS



| Pin | Name | Туре | Description   |
|-----|------|------|---------------|
| 1   | GND  | Р    | Ground        |
| 2   | GND  | Р    | Ground        |
| 3   | +VDC | Р    | DC-IN POWER + |
| 4   | +VDC | Р    | DC-IN POWER + |

## 2.4.3 J16/J17 Ethernet connector (3)

Specification: RJ45, supporting 10M/100M/1000M Base-T, LED: L-Y; R-G



Pinout description:

| Pin | Name     | Туре | Description           |
|-----|----------|------|-----------------------|
| 1   | L_MDI_0P | IO   | Ethernet MDI0+ Signal |
| 2   | L_MDI_0N | IO   | Ethernet MDIO- Signal |
| 3   | L_MDI_1P | IO   | Ethernet MDI1+ Signal |
| 4   | L_MDI_1N | IO   | Ethernet MDI1- Signal |
| 5   | L_MDI_2P | IO   | Ethernet MDI2+ Signal |
| 6   | L_MDI_2N | IO   | Ethernet MDI2- Signal |
| 7   | L_MDI_3P | IO   | Ethernet MDI3+ Signal |
| 8   | L_MDI_3N | Ю    | Ethernet MDI3- Signal |

#### 2.4.4 J6 LVDS connector (4)

Specification: 2 x 15 x 2.0mm, 1.5A, 6mm (H), Male, Vertical, WDT, SMT, RoHS



| Pin | Name         | Туре | Description         |
|-----|--------------|------|---------------------|
| 1   | VDD_LCD      | Р    | LCD POWER +5V       |
| 2   | VDD_LCD      | Р    | LCD POWER +5V       |
| 3   | VDD_LCD      | Р    | LCD POWER +5V       |
| 4   | NC           |      |                     |
| 5   | LCD_DETECT_R | I    | LVDS DETECT         |
| 6   | SEL 6/8      | 0    | SELECT 6 OR 8 DEPTH |
| 7   | LVDS_A_D0R   | 0    | LVSDO_DATA          |
| 8   | LVDS_A_D0+_R | 0    | LVSDO_DATA          |
| 9   | LVDS_A_D1R   | 0    | LVSDO_DATA          |
| 10  | LVDS_A_D1+_R | 0    | LVSDO_DATA          |

| 11 | LVDS_A_D2R       | 0 | LVSDO_DATA  |
|----|------------------|---|-------------|
| 12 | LVDS_A_D2+_R     | 0 | LVSDO_DATA  |
| 13 | GND              | Р | Ground      |
| 14 | GND              | Р | Ground      |
| 15 | LVDS_A_CLKR      | 0 | LVSDO_CLOCK |
| 16 | LVDS_A_CLK+_R    | 0 | LVSDO_CLOCK |
| 17 | LVDS_A_D3R       | 0 | LVSDO_DATA  |
| 18 | LVDS_A_D3+_R     | 0 | LVSDO_DATA  |
| 19 | LVDS_B_D0-/TX0-  | 0 | LVSAE_DATA  |
| 20 | LVDS_B_D0+/TX0+  | 0 | LVSAE_DATA  |
| 21 | LVDS_B_D1-/TX1-  | 0 | LVSAE_DATA  |
| 22 | LVDS_B_D1+/TX1+  | 0 | LVSAE_DATA  |
| 23 | LVDS_B_D2-/TX2-  | 0 | LVSAE_DATA  |
| 24 | LVDS_B_D2+/TX2+  | 0 | LVSAE_DATA  |
| 25 | GND              | Р | Ground      |
| 26 | GND              | Р | Ground      |
| 27 | LVDS_B_CLK-/AUX- | 0 | LVSAE_CLOCK |
| 28 | LVDS_B_CLK+/AUX+ | 0 | LVSAE_CLOCK |
| 29 | LVDS_B_D3-/TX3-  | 0 | LVSAE_DATA  |
| 30 | LVDS_B_D3+/TX3+  | 0 | LVSAE_DATA  |

## 2.4.5 J10 LCD BKL connector (5)

Specification: 1 x 6, 2.0mm, 2A, 6mm (H), Male, Vertical, WDT, THR, RoHS



| Pin | Name         | Туре | Description              |
|-----|--------------|------|--------------------------|
| 1   | VCC_BLK      | Р    | LCD BACKLIGHT POWER +12V |
| 2   | VCC_BLK      | Р    | LCD BACKLIGHT POWER +12V |
| 3   | LCD_BKLTEN   | Р    | LCD BACKLIGHT ENABLE     |
| 4   | LCD_BKLT_PWM | 0    | LCD BACKLIGHT PWM        |
| 5   | GND          | Р    | Ground                   |
| 6   | GND          | Р    | Ground                   |

# 2.4.6 U17 HDMI (6)

Specification: Type-A, FLN, Female, Right angle, WDT, SMT, RoHS



| Pin | Name         | Туре | Description             |
|-----|--------------|------|-------------------------|
| 1   | HDMI_DATA2+  | 0    | HDMI DATA               |
| 2   | GND          | Р    | Ground                  |
| 3   | HDMI_DATA2-  | 0    | HDMI DATA               |
| 4   | HDMI_DATA1+  | 0    | HDMI DATA               |
| 5   | GND          | Р    | Ground                  |
| 6   | HDMI_DATA1-  | 0    | HDMI DATA               |
| 7   | HDMI_DATA0+  | 0    | HDMI DATA               |
| 8   | GND          | Р    | Ground                  |
| 9   | HDMI_DATA0-  | 0    | HDMI DATA               |
| 10  | HDMI_CLK+    | 0    | HDMI CLK                |
| 11  | GND          | Р    | Ground                  |
| 12  | HDMI_CLK-    | 0    | HDMI CLK                |
| 13  | NC           |      |                         |
| 14  | NC           |      |                         |
| 15  | HDMI_DDC_SCL | Ю    | HDMI DDC I2C CLK        |
| 16  | HDMI_DDC_SDA | Ю    | HDMI DDC I2C DATA       |
| 17  | GND          | Р    | Ground                  |
| 18  | VCC_HDMI     | Р    | HDMI POWER +5V          |
| 19  | HDMI HPD     | I    | HDMI HOT PLUG DETECTION |

# 2.4.7 J11 VGA (7)

#### Specification: DB15, 1 Port, NUF, Female, Right angle, WDT, SMT, RoHS



Pinout description:

| Pin | Name         | Туре | Description |
|-----|--------------|------|-------------|
| 1   | VGA_R        | 0    |             |
| 2   | VGA_G        | 0    |             |
| 3   | VGA_B        | 0    |             |
| 4   | NC           |      |             |
| 5   | GND          | Р    | Ground      |
| 6   | GND          | Р    | Ground      |
| 7   | GND          | Р    | Ground      |
| 8   | GND          | Р    | Ground      |
| 9   | +V5_CRT      | Р    | POWER +5V   |
| 10  | GND          | Р    | Ground      |
| 11  | NC           |      |             |
| 12  | VGA_DDC_DATA | 0    |             |
| 13  | VGA_HS       | 0    |             |
| 14  | VGA_VS       | 0    |             |
| 15  | VGA_DDC_CLK  | 0    | VGA CLK     |

## 2.4.8 J19/J18 RS232 port (8)

VT-MITX-APL implements two RS232 serial ports.

Specification: 2 x 5 x 1.5mm, 5.75mm (H), Male, Vertical, Black, WDT, THR, RoHS



| Pin | Name   | Туре | Description |
|-----|--------|------|-------------|
| 1   | DCD4_L | Р    | POWER       |
| 2   | RXD4_L | I    | RS232_RXD   |
| 3   | TXD4_L | 0    | RS232_TXD   |
| 4   | DTR4   | I/O  | DTR4        |
| 5   | GND    | Р    | Ground      |
| 6   | DSR4   | I/O  | DSR4        |
| 7   | RTS4   | I/O  | RTS4        |
| 8   | CTS4   | I/O  | CTS4        |
| 9   | RI4_L  | I/O  | RI4_L       |

#### Pinout description:

### 2.4.9 J20/J21 RS232/RS422/RS485 port (9)

Next to the two RS232 serial ports are another two serial ports that are used as RS232/RS422/RS485.

Specification: 2 x 5 x 1.5mm, 5.75mm (H), Male, Vertical, Black, WDT, THR, RoHS



| Pin | Name           | Туре | Description |
|-----|----------------|------|-------------|
| 1   | RS485_A/422TX+ | Ю    | RS485_P     |
| 2   | RS485_B/422TX- | Ю    | RS485_N     |
| 3   | 422RX+         | Ю    | DATA        |
| 4   | 422RX-         | Ю    | DATA        |
| 5   | GND            | Р    | GND         |
| 6   | DSR1           | Ю    | DATA        |
| 7   | RTS1           | Ю    | DATA        |
| 8   | CTS1           | Ю    | DATA        |
| 9   | RI1_L          | Ю    | DATA        |

## 2.4.10 J39 GPIO (10)

VT-MITX-APL implements a GPIO connector, offering 8 GPIO signals.

Specification: 2 x 5, 2.0mm, 1.5A, 4mm (H), Male, Vertical, WDT, THR, RoHS



Pinout description:

| Pin | Signal      | Туре | Description     |
|-----|-------------|------|-----------------|
| 1   | GPIO_0_3.3V | Ю    | GPIO            |
| 2   | GPIO_1_3.3V | Ю    | GPIO            |
| 3   | GPIO_2_3.3V | IO   | GPIO            |
| 4   | GPIO_3_3.3V | IO   | GPIO            |
| 5   | GPIO_4_3.3V | IO   | GPIO            |
| 6   | GPIO_5_3.3V | IO   | GPIO            |
| 7   | GPIO_6_3.3V | IO   | GPIO            |
| 8   | GPIO_7_3.3V | IO   | GPIO            |
| 9   | GND         | IO   | GPIO            |
| 10  | VCC_GPIO    | IO   | +3.3V/+5V POWER |

#### 2.4.11 J40 Power/reset connector (11)

Specification: 2 x 4, 2.54mm, 2A, 6mm (H), Male, Vertical, WDT, THR, RoHS



| Pin | Name      | Туре | Description |
|-----|-----------|------|-------------|
| 1   | SATA_ACT+ | Ю    | SATA_ACT+   |
| 2   | LED_POWER | 0    | LED_POWER   |
| 3   | SATA_ACT# | Ю    | SATA_ACT+   |
| 4   | GND       | Р    | GND         |

| 5 | GND       | Р | GND          |
|---|-----------|---|--------------|
| 6 | PBTN_IN#  | I | Power Button |
| 7 | SYS_REST# | I | SYS_REST     |
| 8 | GND       | Р | GND          |

#### 2.4.12 J25 M.2 B-Key slot (12)

VT-MITX-APL implements an M.2 B-Key that supports a size of 2242 and is compatible with PCIe x4/SATA to connect an SSD for huge data transfer and storage. The slot also supports a size of 3052 and is compatible with PCIe x1/USB 3.1 to connect a 5G module for faster wireless communication.

Specification: Key B, 75P, 0.5mm, 6.7mm (H), WDT, SMT, RoHS

The pinout of the M.2 B-Key slot is in line with the pin assignment of standard M.2 slot for Key B.

#### 2.4.13 J24 M.2 E-Key slot (13)

VT-MITX-APL implements an M.2 E-Key (2230) that is compatible with PCIe x1/USB 2.0 to connect a Wi-Fi & BT module for wireless communication.

Specification: Key E, 75P, 0.5mm, 6.7mm (H), WDT, SMT, RoHS

The pinout of the M.2 E-Key slot is in line with the pin assignment of standard M.2 for Key E.

#### 2.4.14 J23 Mini PCIe slot (14)

VT-MITX-APL also implements a mini PCIe slot for a 4G/LTE module.

Specification: Mini PCIe, 52P, 0.8mm, 6.8mm (H), WDT, SMT, RoHS

The pinout of the mini PCIe slot is in line with the pin assignment of standard mini PCIe slot.

#### 2.4.15 J16 SATA connector (15)

The SATA connector is designed to connect a storage device for capacity expansion.

Specification: 7-pin, 1.27mm, 8.4mm (H), WDT, SMT, RoHS

The pinout of the SATA connector is in line with the pin assignment of standard SATA port.

## 2.4.16 J17 SATA power connector (16)

VT-MITX-APL implements a 4-pin power connector to supply power to the SATA device.

Specification: 1 x 4, 2.54mm, 2A, 6mmH, Male, Vertical, WDT, THR, RoHS



Pinout description:

| Pin | Name   | Туре | Description |
|-----|--------|------|-------------|
| 1   | +V5_S  | Р    | POWER +5V   |
| 2   | GND    | Р    | Ground      |
| 3   | GND    | Р    | Ground      |
| 4   | +V12_S | Р    | POWER +12V  |

#### 2.4.17 J29 USB 2.0 port (17)

VT-MITX-APL provides two USB 2.0 ports designed to connect peripherals to expand the functions.

Specification: 2.0, Type-A, Female, Right angle, Retention, WDT, THR, RoHS



The pinout of the USB 2.0 port is in line with the pin assignment of standard USB 2.0 connector.

### 2.4.18 U46 USB 3.0 port (18)

VT-MITX-APL provides two USB 3.0 ports for expansion of functions.

Specification: 3.0, Type-A, Female, 17.5mm (L), Right angle, WDT, THR, RoHS



The pinout of the USB 3.0 port is in line with the pin assignment of standard USB 3.0 connector.

### 2.4.19 J31/J33 USB2.0 pin header (19)

The Board implements two USB pin headers to allow users for custom development.

Specification: 2 x 5, 2.54mm, 2A, 6mm (H), Male, Vertical, WDT, THR, RoHS



| Pin | Name           | Туре | Description              |
|-----|----------------|------|--------------------------|
| 1   | VCC_USB2.0_HDR | Р    | USB POWER +5V            |
| 2   | VCC_USB2.0_HDR | Р    | USB POWER +5V            |
| 3   | HUB_USB4N      | Ю    | Reserved usb2.0 Negative |
| 4   | HUB_USB3N      | Ю    | Reserved usb2.0 Positive |
| 5   | HUB_USB4P      | Ю    | Reserved usb2.0 Negative |
| 6   | HUB_USB3P      | Ю    | Reserved usb2.0 Positive |
| 7   | GND            | Р    | Ground                   |
| 8   | GND            | Р    | Ground                   |
| 10  | NC             |      |                          |

## 2.4.20 J3 SMBUS connector (20)

Specification: 1 x 4,1.25mm, 1A, 4.6mm (H), Male, Vertical, THR, RoHS



Pinout description:

| Pin | Name         | Туре | Description |
|-----|--------------|------|-------------|
| 1   | +V3.3_A      | Р    | POWER       |
| 2   | SMB_SCL_3.3V | 0    | SMB_SCL     |
| 3   | SMB_SDA_3.3V | 0    | SMB_SDA     |
| 4   | GND          | Р    | Ground      |

### 2.4.21 J38 Debug connector (21)

VT-MITX-APL implements a debug connector for debugging and troubleshooting purposes.



| Pin | Name         | Туре | Description |
|-----|--------------|------|-------------|
| 1   | LPC_FRAME#   | Ю    | LPC         |
| 2   | LPC_AD3      | Ю    | LPC         |
| 3   | LPC_AD2      | Ю    | LPC         |
| 4   | LPC_AD1      | Ю    | LPC         |
| 5   | LPC_AD0      | Ю    | LPC         |
| 6   | GND          | Р    | Ground      |
| 7   | LPC_CLK1_25M | Ю    | LPC         |
| 8   | +V3.3_A      | Р    | +3.3V POWER |

## 2.4.22 J12 Audio jack (22)

Specification: 3.5mm, 5-pole, Female, Right angle, THR, RoHS



Pinout description:

| Pin | Name            | Туре | Description            |
|-----|-----------------|------|------------------------|
| 1   | GND             | Р    | Ground                 |
| 2   | HPOUT_L_CRL     | 0    | AUDIO JACK LEFT VOICE  |
| 3   | HPOUT_R_CRL     | 0    | AUDIO JACK RIGHT VOICE |
| 4   | ALOUT_L_SPEAKER | I.   | LEFT SPEAKER INPUT     |
| 5   | ALOUT_R_SPEAKER | I.   | RIGHT SPEAKER INPUT    |

## 2.4.23 J14 Microphone jack (23)

Specification: 3.5mm, 5-pole, Female, Right angle, THR, RoHS



| Pin | Name     | Туре | Description     |
|-----|----------|------|-----------------|
| 1   | GND      | Р    | Ground          |
| 2   | MIC1_RRR | I    | MIC RIGHT INPUT |
| 3   | MIC1_LLL | I    | MIC LEFT INPUT  |
| 4   | GND      | Р    | Ground          |
| 5   | MIC_JD   | I    | JD INPUT        |

## 2.4.24 J13 Speaker connector (24)

Specification: 1 x 4, 2.54mm, 4A, 10.8mm (H), Male, Vertical, THR, RoHS



Pinout description:

| Pin | Name   | Туре | Description            |
|-----|--------|------|------------------------|
| 1   | OUTPL+ | 0    | 8R/15W SPEAKER ANODE   |
| 2   | OUTPL- | 0    | 8R/15W SPEAKER CATHODE |
| 3   | OUTPR- | 0    | 8R/15W SPEAKER CATHODE |
| 4   | OUTPR+ | 0    | 8R/15W SPEAKER CATHODE |

#### 2.4.25 J50 Front panel audio connector (25)

Specification: 2 x 5, 2.54mm, 3A, 6mm (H), Male, Vertical, THR, RoHS



| Pin | Name      | Туре | Description      |
|-----|-----------|------|------------------|
| 1   | MIC2_LLL  | I.   | MIC LEFT INPUT   |
| 2   | GND       | Р    | Ground           |
| 3   | MIC2_RRR  | I.   | MIC RIGHT INPUT  |
| 4   | NC        |      |                  |
| 5   | RINP_AMP2 | 0    | AMP2 RIGHT INPUT |
| 6   | MIC2_JD   | I.   | MIC2 JD INPUT    |
| 7   | GND       | Р    | Ground           |
| 9   | LINP_AMP2 | 0    | AMP2 LEFT INPUT  |
| 10  | HP2_JD    | I.   | HP2 JD INPUT     |

## 2.4.26 B1 RTC connector (26)

Specification: 24mm (D), Female, Right angle, WDT, THR, RoHS



Pinout description:

| Pin | Name    | Туре | Description |
|-----|---------|------|-------------|
| 1   | BAT_PWR | Р    | RTC +       |
| 2   | GND     | Р    | RTC -       |

#### 2.4.27 J38 PS/2 (27)

VT-MITX-APL implements a PS/2 connector for connecting a keyboard or mouse.

Specification: 1 x 6, 2.0mm, 2A, 6mm (H), Male, Vertical, WDT, THR, RoHS



#### Pinout description:

| Pin | Name         | Туре | Description |
|-----|--------------|------|-------------|
| 1   | L_KBD_CLK    | Ю    | KBD_CLK     |
| 2   | L_KBD_DATA   | Ю    | KBD_DATA    |
| 3   | L_MOUSE_CLK  | Ю    | MOUSE_CLK   |
| 4   | GND          | Р    | Ground      |
| 5   | PS_5V        | Р    | +5V POWER   |
| 6   | L_MOUSE_DATA | Ю    | MOUSE_DATA  |

#### 2.4.28 DDR3L SO-DIMM socket (28)

VT-MITX-APL offers a DDR3L SO-DIMM socket, capable of supporting memory modules with a maximum frequency of 1866 MHz and up to 8GB of memory capacity.

## 2.4.29 SIM slot (29)

There is a SIM slot on the Board, allowing it to communicate wirelessly via a cellular network and establish a secure data connection.

## 2.4.30 J36/J37 Fan connectors (30)

VT-MITX-APL implements two fan connectors, one (J37) is to connect a fan to offer active heat dissipation for the system, and the other (J36) is a CPU fan connector for cooling the CPU.

Specification: 1 x 4, 2.54mm, 4A, 11.4mm (H), Male, Vertical, WDT, THR, RoHS

Pinout description of J36:

| Pin | Signal          | Туре | Description        |
|-----|-----------------|------|--------------------|
| 1   | GND             | Р    | Ground             |
| 2   | FAN SUPPLY_+V12 | Р    | +12V POWER         |
| 3   | CPU_TACHO_R_FAN | Ю    | FAN SPEED FEEDBACK |
| 4   | FAN_CONN_PWM_IN | Ю    | FAN SPEED CONTROL  |

Pinout description of J37:

| Pin | Signal          | Туре | Description       |
|-----|-----------------|------|-------------------|
| 1   | GND             | Р    | Ground            |
| 2   | FAN SUPPLY_+V12 | Р    | +12V POWER        |
| 3   | NA              |      |                   |
| 4   | +V5S            | Ю    | FAN SPEED CONTROL |

# **CHAPTER 3 FIRST-USE DEBUGGING**

# 3.1 Serial Configuration

VT-MITX-APL implements 4 serial connectors identified as COM1 ~ COM4 by the device manager shown as follows.

- Portable Devices
   Ports (COM & LPT)
   Communications Port (COM1)
   Communications Port (COM2)
   Communications Port (COM3)
   Communications Port (COM4)
   Processors
- The ports displayed here may not necessarily match the ones that are identified by your device manager, so please be aware of any discrepancies. To differentiate between the ports, connect one serial port to the host PC at a time.

In this case, COM1 and COM2 support RS232, RS485, RS422 protocols, and COM3 and COM4 support RS232 protocol. COM1 ~ COM4 correspond to serial port A, B, C, D in BIOS system.

If you wish to change the mode of COM1 and COM2,

- 1. Enter BIOS;
- 2. Click Device Manager > SIO SCH3222;
- 3. Move the cursor to **Serial Port A / Serial Port B > Mode**, and use the up & down arrows to change the mode;

|                    | \$10 SCH3222                      |
|--------------------|-----------------------------------|
| \$10 SCH3222       |                                   |
|                    |                                   |
| Serial Port A      | <enable></enable>                 |
| Base 1/0 Address   | <3F8>                             |
| Interrupt          | <irq4></irq4>                     |
| Mode               | <r\$232></r\$232>                 |
| F IF0              | <disable></disable>               |
| Interrupt Mode     | <edge></edge>                     |
| Interrupt Polarity | <rising triger=""></rising>       |
| Serial Port B      | <enable></enable>                 |
| Base 1/0 Address   | <2F8>                             |
| Interrupt          | <1RQ3>                            |
| Mode               | <r\$232></r\$232>                 |
| Interrupt Mode     | <edge></edge>                     |
| Interrupt Polarity | <rising triger=""></rising>       |
| Serial Port C      | <enable></enable>                 |
| Base 1/0 Address   | <3E8>                             |
| Interrupt          | <irq5></irq5>                     |
| Interrupt Mode     | <edge></edge>                     |
| Interrupt Polarity | <rising triger=""> 🛛 🕅 🤇</rising> |
| Serial Port D      | <enable></enable>                 |
| Base 1/0 Address   | <2E8>                             |
| Interrupt          | <1RQ6>                            |
| Interrupt Mode     | <edge></edge>                     |
| Interrupt Polarity | <rising triger=""></rising>       |
| WDT                | <enable></enable>                 |
| WDT Count Mode     | <second></second>                 |
| Counter            | [0]                               |

4. Press **F10** to save and exit.

You can then use the **TestCommPC Vxxx** tool in the directory of **SW Guide > COM test** in the release package for serial debugging.

# 3.2 GPIO Setup

| Name   | Default mode | Default level |
|--------|--------------|---------------|
| GPIO_0 | Output       | High          |
| GPIO_1 | Input        | /             |
| GPIO_2 | Output       | High          |
| GPIO_3 | Input        | /             |
| GPIO_4 | Output       | High          |
| GPIO_5 | Input        | /             |
| GPIO_6 | Output       | High          |
| GPIO_7 | Input        | /             |

VT-MITX-APL implements 8 GPIO pins with details shown below:

You can use the **GPIO Sample.exe** program under the directory of **SW Guide > GPIO Test > GPIO Sample** in the release package for GPIO debugging (to run as administrator).



In the above figure:

- 1. GpioPins: You can select a pin of the GPIO header from the drop-down list for the configuration;
- 2. Out: Set the mode of the selected GPIO pin as output (checked)/input (unchecked);
- 3. High: Set the level of the selected GPIO pin as high (checked)/low (unchecked).
- Please run this program as administrator.

# 3.3 Enabling Watchdog Timer

If you need enable the Watchdog Timer,

- 1. Enter BIOS;
- 2. Click Device Manager > SIO SCH3222;
- Move the cursor to WDT > Enable (Disabled by default), then set the Count Mode and Counter (time length);

| Mode               | <r\$232></r\$232>           |
|--------------------|-----------------------------|
| Interrupt Mode     | <edge></edge>               |
| Interrupt Polarity | <rising triger=""></rising> |
| WDT                | <enable></enable>           |
| WDT Count Mode     | <second></second>           |
| Counter            | [0]                         |

- The Counter shall be set more than 80 if the Count Mode is second;
- The Counter shall be set more than **3** if the Count Mode is **minute**;
- 4. Press F10 to save the settings;
- 5. Press Ctrl + Alt + Delete to restart the system to get the settings take effect.

# **CHAPTER 4 BIOS AND WINDOWS**

# 4.1 **BIOS Introduction**

BIOS initializes hardware like CPU and memory, and saves hardware settings for installation and loading of the operating system (OS).

Users may need to run BIOS Setup program when:

- An error message appears suggesting that the user should run BIOS Setup;
- Default settings need to be customized.
- Please be aware that BIOS will be under continuous update for better system performance, therefore the description in this chapter might vary slightly and is for reference only.

Make sure to connect a keyboard, a mouse and a display to the Board before you proceed with any further operations.

## 4.2 Check BIOS Version

The Board supports Windows operating system. You can check the BIOS version of the Board in Windows system in accordance with the following steps:

- 1. Press "Win + R" on the keyboard to call the command box;
- 2. Input msinfo32 in the command box and click "OK" to confirm;

| 🖅 Run         | X   |
|---------------|---|
| <b>O</b> pen: | Type the name of a program, folder, document, or Internet resource, and Windows will open it for you. |
|               | OK Cancel <u>B</u> rowse  |

3. Move to BIOS Version/Date on the open page to check the detailed information.

| ltem                        | Value   |
|-----------------------------|---|
| OS Name                     | Microsoft Windows 10 Enterprise LTSC  |
| Version                     | 10.0.17763 Build 17763  |
| Other OS Description        | Not Available   |
| OS Manufacturer             | Microsoft Corporation   |
| System Name                 | DESKTOP-0QGJIDR   |
| System Manufacturer         | Insyde  |
| System Model                | ApolloLake  |
| System Type                 | x64-based PC  |
| System SKU                  | Type1 - SKU0  |
| Processor                   | Intel(R) Celeron(R) CPU N3350 @ 1.10GHz, 1101 Mhz, 2 Core(s), 2 Logical Pro |
| BIOS Version/Date           | INSYDE Corp. RBXC09B011_v1.0, 2/5/2021                                      |
| SMBIOS Version              | 3.0   |
| Embedded Controller Version | 0.00  |
| BIOS Mode                   | UEFI  |
| BaseBoard Manufacturer      | Vantron Technology,Inc.   |
| BaseBoard Product           | Vantron MITX-APL  |
| BaseBoard Version           | 1.0   |
| Platform Role               | Mobile  |
| Secure Boot State           | Off   |
|                             |   |

# 4.3 BIOS Setup

## 4.3.1 Entering Setup

Power on the Board and the system will start the power-on self-test process. Then press the **ESC** key to enter BIOS configuration page (front page) as shown below.

| Front Page   |   |
|--|---|
| Front Page   |   |
| Continue<br>+Boot Manager<br>+Device Manager<br>+Boot From File<br>+Administer Secure Boot<br>+Setup Utility | This selection will direct the system to<br>continue to booting process |

#### Description of the options:

| Option                 | Description  |
|------------------------|--|
| Continue               | Proceed with the booting process   |
| Boot Manager           | View all boot devices, including USB drives, SSD, etc.   |
| Boot From File         | Choose to boot from an internal file, only for EFI partition   |
| Administer Secure Boot | Configure secure boot function, and enable/disable secure boot                                       |
| Setup Utility          | Overview of all BIOS setup options. You must be very<br>careful when modifying the default settings. |

#### 4.3.2 Secure Boot

Secure Boot is firmware-dependent and requires that the computer BIOS is set to UEFI mode. It is disabled by default.

- 1. Power on the Board and press **ESC** to enter BIOS;
- 2. Select Administer Secure Boot on the front page;
- 3. Set Erase all Secure Boot Settings and Restore Secure Boot to Factory Settings to Enabled;



- 4. Press F10 to save and exit;
- 5. There will be a dialog box indicating the system will be reset. Click **OK**, and the system will reboot;
- 6. If you need to disable Secure Boot after that, set **Enforce Secure Boot** to **Disabled**.



#### Check the Secure Boot State in the Windows system:

- 1. Press the "Win + R" on the keyboard to call the command box;
- 2. Input msinfo32 in the command box and click "OK" to confirm;

| 💷 Run | ×   |
|-------|---|
|       | Type the name of a program, folder, document, or Internet resource, and Windows will open it for you. |
| Open: | msinfo32 ~  |
|       | OK Cancel <u>B</u> rowse  |

3. Move to **BIOS Mode** and **Secure Boot State** on the open page to check the detailed information.

| M System Information   |                             |  |
|------------------------|-----------------------------|--|
| Eile Edit View Hele    |                             |  |
| File Edit View Help    |                             |  |
| System Summary         | Item                        | Value  |
| Hardware Resources     | OS Name                     | Microsoft Windows 10 Enterprise LTSC   |
| Components             | Version                     | 10.0.17763 Build 17763   |
| B Software Environment | Other OS Description        | Not Available  |
|                        | OS Manufacturer             | Microsoft Corporation  |
|                        | System Name                 | DESKTOP-BDVC1M5  |
|                        | System Manufacturer         | Insyde   |
|                        | System Model                | ApolloLake   |
|                        | System Type                 | x64-based PC   |
|                        | System SKU                  | Type1 - SKU0   |
|                        | Processor                   | Intel(R) Atom(TM) Processor E3940 @ 1.60GHz, 1601 Mhz, 4 Core(s), 4 Logical. |
|                        | BIOS Version/Date           | INSYDE Corp. RBXC08B101_v1.6, 12/9/2019                                      |
|                        | SMBIOS Version              | 3.0  |
|                        | Embedded Controller Version | 0.00   |
|                        | BIOS Mode                   | UEFI   |
|                        | BaseBoard Manufacturer      | Type2 - Board Manufacturer   |
|                        | BaseBoard Product           | Type2 - Board Product Name   |
|                        | BaseBoard Version           | Type2 - Board Version  |
|                        | Platform Role               | Mobile   |
|                        | Secure Boot State           | Off  |
|                        | PCR7 Configuration          | Elevation Required to View   |
|                        | Windows Directory           | C:\Windows   |
|                        | System Directory            | C:\Windows\system32  |
|                        | Boot Device                 | \Device\HarddiskVolume2  |
|                        | Locale                      | United States  |
|                        | Hardware Abstraction Laver  | Version = "10.0.17763.1"   |

Use the up and down arrow keys on the keyboard to enter BIOS **Setup Utility**, which features the following menus in the menu bar:

- Main (basic system configurations, like BIOS version, processor information, system language, system time and date)
- Advanced (advanced configurations to allow users to customize the system)
- Security (system security settings where users can set supervisor passwords)
- Power (CPU power settings for power management purpose)
- Boot (system boot options)
- Exit (BIOS load or exit options with or without changes saved)

#### 4.3.3 Main

|                                   | InsydeH20           | Setup Utility          | Rev. 5.0                                 |
|-----------------------------------|---------------------|------------------------|--|
| Hain Advanced Security Power Boot | Exit                |                        |  |
| InsydeH20 Version                 | PRXC09R011 v1 1     |                        | Select the current default language used |
| UFFL Version                      | 2 50                |                        | by the InsydeH20                         |
| Product Name                      | VT-mITX-API X64     |                        | by the moydenze.                         |
| Build Date                        | 07/28/2021 12:48:12 | 1                      |  |
|                                   |                     |                        |  |
| Processor Type                    | Intel(R) Celeron(R) | CPU N3350 @ 1.10GHz    |  |
| System Bus Speed                  | 100 MHz             |                        |  |
| System Memory Speed               | 1600 MHz            |                        |  |
| Cache RAM                         | 2048 KB             |                        |  |
| Total Memory                      | 2048 MB             |                        |  |
| Channel A - SODIMM O              | 2048 MB             |                        |  |
| Channel B - SODIMM O              | [Not Installed]     |                        |  |
| Channel C - SODIMM O              | [Not Installed]     |                        |  |
| Channel D - SODIMM O              | [Not Installed]     |                        |  |
| Distory firmers information       |                     |                        |  |
|                                   | E1 Stopping         |                        |  |
| SIC Version                       | 1 1 1 ( IOTG)       |                        |  |
| MDC Version                       | 00 56 (89 24)       |                        |  |
|                                   | 14                  |                        |  |
| PHC EW                            | 03.20               |                        |  |
| TXF FW                            | 3 1 70 2334         |                        |  |
| ISH FW                            | 4, 1, 0, 3364       |                        |  |
| GOP                               | 10.0.1037           |                        |  |
| Board ID                          | Leaf Hill CRB (07)  |                        |  |
| Fab ID                            | FAB A (00)          |                        |  |
| BIOS Boot Source                  | SPI (02) (08 MB)    |                        |  |
|                                   | (English)           |                        |  |
| System Tine                       | [02+28+451          |                        |  |
| System Date                       | [02/08/2016]        |                        |  |
| of ortonic of the                 | 102/00/20101        |                        |  |
|                                   |                     |                        |  |
| F1 Help                           | t Item              | F5/F6 Change Values    | F9 Setup Defaults                        |
| Esc Exit +/→ Selec                | t Item              | Enter Select ► SubMenu | F10 Save and Exit                        |

- Language: You can select from English, French, Chinese, and Japanese for system language.
- System Time: The time format is <Hour>: <Minute>: <Second>.
- System Date: The date format is <Month>/ <Day>/<Year>.

## 4.3.4 Advanced

|  |                                    | InsydeH20 Setup Utility                       | Rev. 5.                                |
|--|------------------------------------|---|--|
| Main Advanced Security Pow   | er Boot Exit                       |   |  |
| Hain         Advanced         Security         Pow           PBoot Configuration         >Juncore Configuration         >South Cluster Configuration           >South Cluster Configuration         >Thermal Configuration         >Thermal Configuration           >Phenu Configuration         >RTD3 settings         >Henory System Configuration           >ACP1         Table/Features         Configuration           >SEG Chipset Feature         >OEH Configuration           >S10 Sch3222         >H20Uve Configuration | er Boot Exit                       | InsydeH20 Setup Utility                       | Configures Boot Settings.              |
| F1 Help<br>Esc Exit  | ↑/↓ Select Item<br>+/→ Select Item | F5/F6 Change Values<br>Enter Select ▶ SubMenu | F9 Setup Defaults<br>F10 Save and Exit |

- Boot Configuration: You can select the operating system that you would like the Board to run on.
- Uncore Configuration: You can customize the video settings, GOP settings, IGD settings, and IPU PCI device settings here.
- South Cluster Configuration: This page provides configuration options for audio, GMM, ISH, LPSS, PCIe, SATA, SCC, USB, Timer, etc.
- Security Configuration: TPM device settings are made here.
- Thermal Configuration: Thermal management settings are customized here.
- System Component: Spread spectrum clocking configurations could be accessed from here.
- Debug Configuration: You can enable/disable the debugger here.
- Memory System Configuration: You can enable/disable the memory scrambler and other memory-related settings here.
- ACPI Table/Features Control: This option allows you to enable/disable S4 wakeup from RTC (only available for ACPI).
- SEG Chipset Feature: This option allows you to enable/disable wakeup on USB from S5 state.
- OEM Configuration: LVDS configurations are available to change.
- SIO SCH 3222: Serial ports are configured here.
- H2OUVE Configuration: You can enable/disable the configuration interface of H2OUVE tool.

## 4.3.5 Security

|   | InsydeH20 Set  | up Utility            | Rev. 5.                           |
|---|--|-----------------------|-----------------------------------|
| Main Advanced Security Power Boot   | Exit   |                       |                                   |
| Current TPM Device<br>TPM State<br>TPM Active PCR Hash Algorithm<br>TPM Hardware Supported Hash Algorithm<br>TPE Protocol Version<br>TPM Availability<br>TPM Operation<br>Clear TPM | <tph (ftph)="" 2.0=""><br/>All Hierarchies Enable<br/>SHA1<br/>SHA1, SHA256<br/>&lt;1.0&gt;<br/><available><br/><no operation=""><br/>[ ]</no></available></tph> | d, UnOwned            | TrEE Protocol Version: 1.0 or 1.1 |
| Supervisor Password   | Not Installed  |                       |                                   |
| Set Supervisor Password   |  |                       |                                   |
| F1 Help 1/4 Sel   | ect Item F5  | /F6 Change Values     | F9 Setup Defaults                 |
| Esc Exit +/+ Sel  | ect Item En  | iter Select 🕨 SubMenu | FIO Save and Exit                 |

• Information of current TPM device is available here and you can set the supervisor passwords as well.

#### 4.3.6 Power

|       |             |          |       |         | In                             | sydeH20 Setup | Utility |           |    |              | Rev. 5.0 |
|-------|-------------|----------|-------|---------|--------------------------------|---------------|---------|-----------|----|--------------|----------|
| Main  | Advanced    | Security | Power | Boot    | Exit                           |               |         |           |    |              |          |
|       |             |          |       |         |                                |               |         |           |    |              |          |
| ►CPU  | Configurati |          |       |         |                                |               |         |           |    |              |          |
|       |             |          |       |         |                                |               |         |           |    |              |          |
| Wake  | on PME      | -5       |       |         | <disabled></disabled>          |               |         |           |    |              |          |
| Wake  | on RTC from | n S5     |       |         | <d ed="" i="" l="" sab=""></d> |               |         |           |    |              |          |
|       |             |          |       |         |                                |               |         |           |    |              |          |
|       |             |          |       |         |                                |               |         |           |    |              |          |
|       |             |          |       |         |                                |               |         |           |    |              |          |
|       |             |          |       |         |                                |               |         |           |    |              |          |
|       |             |          |       |         |                                |               |         |           |    |              |          |
|       |             |          |       |         |                                |               |         |           |    |              |          |
|       |             |          |       |         |                                |               |         |           |    |              |          |
|       |             |          |       |         |                                |               |         |           |    |              |          |
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|       |             |          |       |         |                                |               |         |           |    |              |          |
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|       |             |          |       |         |                                |               |         |           |    |              |          |
|       |             |          |       |         |                                |               |         |           |    |              |          |
|       |             |          |       |         |                                |               |         |           |    |              |          |
|       |             |          |       |         |                                |               |         |           |    |              |          |
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|       |             |          |       |         |                                |               |         |           |    |              |          |
|       |             |          |       |         |                                |               |         |           |    |              |          |
|       |             |          |       |         |                                |               |         |           |    |              |          |
|       |             |          |       |         |                                |               |         |           |    |              |          |
| F1    | eln         |          | ti    | I Selo  | ct Item                        | F5/E          | Change  | Values    | EQ | Setun Defaul | ts       |
| Esc E | xit         |          | +/    | l→ Sele | ct Item                        | Ente          | Select  | ▶ SubMenu | F1 | Save and Exi | t        |

- CPU configurations are customizable.
- Options for wakeup on PME/RTC from S5 are available.

#### 4.3.7 Boot

|  | Ins  | ydeH20 Setup Utility                          | Rev. 5.0   |
|--|--|---|--|
| Main Advanced Security Pow   | er Boot Exit   |   |  |
| Tain Advanced Security Pou<br>Doot Type<br>Quick Boot<br>Quick Boot<br>Retwork Stack<br>PXE Boot capability<br>Power Up In Standby Support<br>Add Boot Options<br>ACP1 Selection<br>USB Boot<br>USB Hoot Key Support<br>USB Hot Key Support<br>Timeout<br>Automatic Failover<br>FEF1 | Yer Boot Exit<br><ul> <li>4UEF   Boot I<br/><pre><pre><pre><pre><pre><pre><pre><pre></pre></pre></pre></pre></pre></pre></pre></pre></li></ul> | ype>  | Select boot type to Dual type, Legacy<br>type or UEFI type |
| F1 Help<br>Esc Exit  | 1/↓ Select Item<br>+/→ Select Item   | F5/F6 Change Values<br>Enter Select ► SubMenu | F9 Setup Defaults<br>F10 Save and Exit                     |

• Users can set the boot mode, the sequence, timeout, and automatic failover of boot devices when BIOS attempts to load the operating system.

#### 4.3.8 Exit

|                            |                 | InsydeH20 Setup Utility | Rev. 5.0                                 |
|----------------------------|-----------------|-------------------------|--|
| Main Advanced Security Pow | wer Boot Exit   |                         |  |
|                            |                 |                         |  |
|                            |                 |                         | Exit system setup and save your changes. |
| Exit Saving Changes        |                 |                         |  |
| Save Change Without Exit   |                 |                         |  |
| Exit Discarding Changes    |                 |                         |  |
| Load Optimal Defaults      |                 |                         |  |
| Load Custom Defaults       |                 |                         |  |
| Save Custom Defaults       |                 |                         |  |
| Discard Changes            |                 |                         |  |
|                            |                 |                         |  |
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|                            |                 |                         |  |
|                            |                 |                         |  |
| FIHelp                     | Select Item     | F5/F6 Change Values     | Fy Setup Defaults                        |
| Esc Exit                   | ←/→ Select Item | Enter Select ► SubMenu  | FIU Save and Exit                        |

• Options for users to load or exit BIOS Setup include loading system optimal defaults or loading custom settings, exiting with custom changes save or not saved.

# 4.4 Windows 10 System Flashing

#### 4.4.1 Prerequisites

- A USB drive with capacity no less than 8GB, preferably supporting USB 3.0
- Bootable USB tool: rufus-xxx .exe (available in the release package under image directory)
- Windows 10 image
- A host PC running Windows 7 or later
- A display cable to connect the Board and the host PC

#### 4.4.2 Making a Bootable USB Drive for Windows 10

Plug the USB drive into the host PC. Run rufus-xxx .exe and it will automatically detect the USB. Then follow the steps below to make a bootable USB drive.

- 1. Click on **Device** and choose the USB you want to use from the drop-down;
- Select the ISO image you want to burn onto the USB from the drop-down and click Select;
- 3. Generally, users would like to create a **Standard Windows installation**, and Rufus will automatically detect the correct **Partition Scheme** based on the USB drive. Yet make sure the partition scheme is **GPT**;
- 4. Set the Target system as **UEFI** and the File system as **FAT32** or **NTFS**;
- 5. Click **START** to make the bootable USB drive.

| Drive Properties   |   |  |        |    |
|--|---|--|--------|----|
| Device   |   |  |        |    |
| CES_X64FREV (I:) [16 GB]   |   |  | ~      |    |
| Boot selection   |   |  |        | -1 |
| win10.iso  |   | × 00   | SELECT |    |
| Image option   |   |  |        |    |
| Standard Windows installation  | on  |  |        | ~  |
| Partition scheme   |   | Target system  |        |    |
| CDT 2  | ~   | UEFI (non CSM)   |        | ~  |
| Hide advanced drive prop     List USB Hard Drives     Add fixes for old BIOSes (e     Use Rufus MBR with BIOS     Format Options   | erties<br>extra partition                       | , align, etc.)<br>0x80 (Detault)   |        | ×  |
| Hide advanced drive prop     List USB Hard Drives     Add fixes for old BIOSes (e     Use Rufus MBR with BIOS     Format Options Volume label     CFS X64EREV_EN-LIS_DV5   | erties<br>xtra partitior<br>ID                  | , align, etc.)<br>0x80 (Default)   |        |    |
| Hide advanced drive prop     List USB Hard Drives     Add fixes for old BIOSes (e     Use Rufus MBR with BIOS     Format Options     Volume label     CES_X64FREV_EN-US_DV5     File system  | erties<br>xtra partitior<br>ID                  | , align, etc.)<br>Ux80 (Default)<br>Cluster size                         |        | 2  |
| Hide advanced drive prop     List USB Hard Drives     Add fixes for old BIOSes (e     Use Rufus MBR with BIOS     Format Options     Volume label     CES_X64FREV_EN-US_DV5     File system     FAT32 (Default)     4                                      | erties<br>extra partition                       | , align, etc.)<br>0x80 (Default)<br>Cluster size<br>8192 bytes (Default) |        | ~  |
| Hide advanced drive prop      List USB Hard Drives      Add fixes for old BIOSes (e      Use Rufus MBR with BIOS      Format Options Volume label      CES_X64FREV_EN-US_DV5      File system     FAT32 (Default)      Show advanced format op      Status | erties<br>xtra partition<br>D<br>v<br>tions     | , align, etc.)<br>0x80 (Default)<br>Cluster size<br>8192 bytes (Default) | ,      | ~  |
| Hide advanced drive prop     List USB Hard Drives     Add fixes for old BIOSes (e     Use Rufus MBR with BIOS     Format Options     Volume label     CES_X64FREV_EN-US_DV5     File system     FAT32 (Default)     Show advanced format op     Status     | erties<br>xtra partition<br>D<br>vitions<br>REA | , align, etc.)<br>0x80 (Default)<br>Cluster size<br>8192 bytes (Default) |        | ~  |

#### 4.4.3 Installing Windows 10

- 1. Plug the bootable USB drive into the Board;
- 2. Connect the Board to the host PC and power the Board on;
- 3. Press F7 to enter the BIOS boot manager menu;
- 4. Select the bootable USB drive you created for Windows 10 and press ENTER;

|   | Boot Manager                     |
|---|----------------------------------|
|   |                                  |
| Boot Option Menu  |                                  |
| EFI Boot Devices<br>EFI USB Device (SMI USB DIS<br>Internal EFI Shell | к)                               |
| † and ↓ to change option, ENTER                                       | to select an option, ESC to exit |

5. Wait for the installation of Windows 10 on the Board. When the installation finishes, there will be a Windows 10 icon on the desktop.

# **CHAPTER 5 DISPOSAL AND WARRANTY**

# 5.1 Disposal

When the device comes to end of life, you are suggested to properly dispose of the device for the sake of the environment and safety.

Before you dispose of the device, please back up your data and erase it from the device.

It is recommended that the device is disassembled prior to disposal in conformity with local regulations. Please ensure that the abandoned batteries are disposed of according to local regulations on waste disposal. Do not throw batteries into fire or put in common waste canister as they are explosive. Products or product packages labeled with the sign of "explosive" should not be disposed of like household waste but delivered to specialized electrical & electronic waste recycling/disposal center.

Proper disposal of this sort of waste helps avoid harm and adverse effect upon surroundings and people's health. Please contact local organizations or recycling/disposal center for more recycling/disposal methods of related products.

# 5.2 Warranty

#### **Product warranty**

VANTRON warrants to its CUSTOMER that the Product manufactured by VANTRON, or its subcontractors will conform strictly to the mutually agreed specifications and be free from defects in workmanship and materials (except that which is furnished by the CUSTOMER) upon shipment from VANTRON. VANTRON's obligation under this warranty is limited to replacing or repairing at its option of the Product which shall, within <u>24</u> <u>months</u> depending on the Product after shipment, effective from invoice date, be returned to VANTRON's factory with transportation fee paid by the CUSTOMER and which shall, after examination, be disclosed to VANTRON's reasonable satisfaction to be thus defective. VANTRON shall bear the transportation fee for the shipment of the Product to the CUSTOMER.

### **Out-of-Warranty Repair**

VANTRON will furnish the repair services for the Product which are out-of-warranty at VANTRON's then-prevailing rates for such services. At customer's request, VANTRON will provide components to the CUSTOMER for non-warranty repair. VANTRON will provide this service as long as the components are available in the market; and the CUSTOMER is requested to place a purchase order up front. Parts repaired will have an extended warranty of 3 months.

### **Returned Products**

Any Product found to be defective and covered under warranty pursuant to Clause above, shall be returned to VANTRON only upon the CUSTOMER's receipt of and with reference to a VANTRON supplied Returned Materials Authorization (RMA) number. VANTRON shall supply an RMA, when required within three (3) working days of request by the CUSTOMER. VANTRON shall submit a new invoice to the CUSTOMER upon shipping of the returned products to the CUSTOMER. Prior to the return of any products by the CUSTOMER due to rejection or warranty defect, the CUSTOMER shall afford VANTRON the opportunity to inspect such products at the CUSTOMER's location and no Product so inspected shall be returned to VANTRON unless the cause for the rejection or defect is determined to be the responsibility of VANTRON. VANTRON shall in turn provide the CUSTOMER turnaround shipment on defective Product within **fourteen (14) working days** upon its receipt at VANTRON. If such turnaround cannot be provided by VANTRON due to causes beyond the control of VANTRON, VANTRON shall document such instances and notify the CUSTOMER immediately.